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Docket No.: 1163-268P

BOX PATENT APPLICATION

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. § 1.53(b),
is a Continuation application of PCT International Application
No. PCT/JP98/00941 filed on March 6, 1998.

Inventor(s): KURODA, Shinichi; SEKIGUCHI, Shunichi;
ASAI, Kohtaro; NISHIKAWA, Hirofumi; ISU, Yoshimi;
HASEGAWA, Yuri

For: IMAGE ENCODING DEVICE AND IMAGE DECODING DEVICE

Enclosed are:

- ☒ A specification consisting of Fifty-Seven (57) pages
- ☒ Twenty-Nine (29) sheet(s) of formal drawings
- ☒ An assignment of the invention
- ☐ Certified copy of Priority Document(s)
- ☒ Executed Declaration ☒ Original ☐ Photocopy
- ☐ A statement (☐ Original ☐ Photocopy) to establish small
entity status under 37 C.F.R. § 1.9 and 37 C.F.R. § 1.27
- ☒ Information Disclosure Statement, PTO-1449 with reference(s)

- ☐ Preliminary Amendment
- ☒ Priority of PCT International Application No. PCT/JP97/03785 filed on October 20, 1997 is claimed under 35 U.S.C. § 120.
- ☐ Amend the specification by inserting before the first line thereof the following:

--This application is a Continuation of PCT International Application No. _____ filed on _____, which designated the United States, and on which priority is claimed under 35 U.S.C. § 120, the entire contents of which are hereby incorporated by reference.--

- ☐ Priority of Application No. _____ filed in _____ on _____ is claimed under U.S.C. § 119.
- ☐ Other:

The filing fee has been calculated as shown below:

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INDEPENDENT CLAIMS	10- 3 =	7	x 78 =	\$546.00	x 39 =	\$0.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS PRESENTED			+ \$260.00		+ \$130.00	
TOTAL			1236.00			

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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By

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Attachments

{Rev. 01/08/2000}

IMAGE ENCODING DEVICE AND IMAGE DECODING DEVICE

CROSS-REFERENCE TO THE RELATED APPLICATION

This application is a continuation of International Application No. PCT/JP98/00941, whose international filing date is March 6, 1998, the disclosures of which Application are incorporated by reference herein.

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to an image encoding device and an image decoding device which perform image processing.

Description of the Prior Art

Conventionally, it is always necessary at the decoding side that the analysis of VOP (Video Object Plane) header information be preceded by analysis of a VOP start code, a modulo time base and a VOP time increment contained in each VOP header, because no distinction can be made between VOPs not to be analyzed (information to be decimated in the case of a low speed shot of an image signal) and those to be analyzed (information not to be decimated). Accordingly, the decoding process is inevitably cumbersome and prone to low accuracy.

For decoding and synthesizing encoded signals respectively corresponding to a subject, a background, a logo and similar objects which form a pictorial image, it is necessary that each object be added with a synthesizing timing signal (information representing absolute time) necessary for decoding and synthesizing the object. Without such absolute time information, the image decoding device cannot synthesize the object, and hence it is incapable of image reconstruction. In short, in the case of generating one pictorial image from a plurality of objects including those having no absolute time information, it is impossible with the prior art to combine objects having the required information with those having no such information.

Moreover, the bit length of the modulo time base increases until the next

GOV header is multiplexed--this raises a problem that the bit length of the modulo time base keeps on increasing when the GOV header, which is an option, is not multiplexed.

SUMMARY OF THE INVENTION

It is therefore an object of the present invention to provide an image encoding and an image decoding device which are free from the abovesaid defects of the prior art and highly accurate in image processing but simple-structured.

Another object of the present invention is to provide an image encoding and an image decoding device which permit the generation of a pictorial image composed of a plurality of objects based on a time code.

Still another object of the present invention is to an image encoding and an image decoding device which perform required image processing with only limited amounts of information necessary therefor.

According to an aspect of the present invention, an image encoding device which encodes images object by object is provided with: encoding means for encoding each image on the basis of predetermined object display speed information; and multiplexing means for multiplexing the predetermined object display speed information onto the encoded image signal.

According to another aspect of the present invention, the object display speed information is multiplexed onto the encoded image signal for each object.

According to another aspect of the present invention, information indicating whether the display speed of an object is fixed or variable and information indicating the value of the display speed are multiplexed together onto the encoded image signal on an object-by-object basis.

According to another aspect of the present invention, the object display speed information contains a value representing an inherent fixed display speed and a value representing an arbitrary fixed display speed. This enables the display speed information decoding side to instantaneously specify the corresponding VOP.

According to another aspect of the present invention, an image decoding device which decodes a bit stream obtained by encoding images object by object is provided with: display speed information decoding means for

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decoding the object display speed information from the bit stream; and control means for controlling the reconstruction of the images processed for each object on the basis of the display speed information decoded by the display speed information decoding means. This permits smooth and accurate image restoration processing with a simple structure.

According to another aspect of the present invention, the object display speed information is decoded for each object. This provides increased smoothness and increased accuracy in the image restoration processing with a simple structure.

According to another aspect of the present invention, the display speed decoding means decodes the object display speed information from the encoded bit stream for each object, and the control means controls the image reconstruction processing, depending on whether the object display speed indicated by the decoded display speed information is a fixed or variable speed. This provides further enhanced smoothness and further increased accuracy in the image restoration processing with a simple structure.

According to another aspect of the present invention, the display speed information decoding means decodes the display speed information from the encoded bit stream for each object, and the control means controls the image reconstruction processing by specifying each image display time on the basis of the information representing the object display speed when the decoded display speed information indicates a fixed speed, and on the basis of display time information multiplexed for each image at each time when the decoded display speed information indicates a variable speed. This also ensures smoother and more accurate image restoration processing with a simple structure.

According to another object of the present invention, the display speed information decoding means decodes the display speed information from the encoded bit stream for each object, and the control means controls the image reconstruction processing by specifying each image display time: on the basis of the display speed information when the decoded display speed information indicates a fixed speed and the fixed speed has the value given by the display speed information; on the basis of display time information multiplexed image by image at each time when the display speed information indicates a fixed speed and the value of the fixed speed is not given as a particular value; and on the basis of display time information

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multiplexed image by image at each time when display speed information indicates a variable speed. This also makes it possible to perform the image restoration processing more smoothly and more accurately with a simple structure.

According to another aspect of the present invention, the control means is provided with: decoding time specifying means for specifying the time at which to decode an object, on the basis of the decoded object display speed information and object display speed information preset in the decoding device; and decoding means for decoding at the time specified by the decoding time specifying means. This also makes the image restoration processing smoother and more accurate with a simple structure.

According to another aspect of the present invention, an image encoding device which encodes images for each object is provided with absolute time multiplexing means by which information representing the absolute time of each object is multiplexed onto the encoded image signal. By this, the information indicating the absolute time can be sent as being multiplexed onto the encoded image signal.

According to another aspect of the present invention, an image decoding device which decodes a bit stream formed by encoding images for each object has absolute time analysis means for analyzing the information indicative of the absolute time of each object, and reconstructs the images processed for each object on the basis of the analyzed absolute time information. This permits implementation of simple and accurate image synthesis processing.

According to another aspect of the present invention, an image encoding device which encodes images for each object is provided with time information encoding means which encodes, as information defining each image display time for each object, first time information defining the time interval between a reference time and the display time, second time information defining the display time with higher accuracy than the time defined by the first time information and the image corresponding to each time. The time information encoding means provides the first time information as a bit length. When the bit length of the first time information is longer than a preset value, it is repeatedly shifted the number of bits specified by the preset value until the former becomes shorter than the latter, and at the same time a count is taken of bit shifts

performed. The time information encoding means encodes the repeat count and a bit string resulting from the repeated bit shift. This permits reduction of the amount of encoded information to send.

According to another aspect of the present invention, an image encoding device which encodes images for each object is provided with time information encoding means which encodes, as information defining each image display time for each object, first time information defining the time interval between a reference time and the display time, second time information defining the display time with higher accuracy than the time defined by the first time information and the image corresponding to each time. The time information encoding means has first time information holding means for holding the first time information encoded for an image at the immediately preceding time. The information encoding means obtains a bit string corresponding to the difference between the first time information of the currently encoded image and the first time information of the immediately preceding image obtainable from the first time information holding means, and encodes the difference bit string as the first time information of the currently encoded image. This ensures reduction of the amount of encoded information to send.

According to another aspect of the present invention, an image decoding device which decodes a bit stream obtained by encoding images for each object, is provided with time information decoding means for decoding, as information defining the display time of an image at each time for each object, first time information defining the time interval between a reference time and said display time and second time information defining the display time with higher accuracy than by the first time information and an image corresponding to each time; and decoding and synthesizing means for decoding input encoded image signals for each object and for synthesizing the decoded image signals. The time information decoding means decodes, as encoded data of the first time information, a count of bit shifts of said first time information and a bit string obtained by repeated bit shifts and decodes the first time information by adding a code of a length of a predetermined set value to the bit string by the count of bit shifts. And the decoding and synthesizing means synthesizes a decoded image signal on the basis of the first and second time information decoded by the time information decoding means. With this structure, it is possible to receive

an image sent with a small amount of encoded information.

According to another aspect of the present invention, an image decoding device which decodes a bit stream formed by encoding an image for each object, is provided with: time information decoding means for decoding, as information defining the display time of an image at each time in an image sequence, first time information defining the time interval between a reference time and the display time and second time information defining the display time with higher accuracy than by the first time information and an image corresponding to each time; and decoding and synthesizing means for decoding input encoded image signals for each object and for synthesizing the decoded image signals. The time information decoding means includes first time information holding means for holding the first time information of an image encoded at immediately preceding time, and decodes the first time information of an image to be decoded after adding a bit string decoded as the first time information of the image to be decoded with the first time information of the immediately previously decoded image available from the first time information holding means. And the decoding and synthesizing means synthesizes the decoded image signals on the basis of the first and second time information decoded by the time information decoding means. This structure also permits reception of an image sent with a small amount of encoded information.

According to another aspect of the present invention, an encoded image signal is generated by encoding an image for each object on the basis of predetermined display speed information, and the encoded image signal and the display speed information are multiplexed onto each other. This permits reduction of the amount of encoded information to send.

According to still another object of the present invention, the display speed information is decoded from a bit stream formed by encoding images for each object, and the images are decoded by controlling their reconstruction on the basis of the decoded display speed information. This permits reception of images sent with a small amount of encoded information.

BRIEF DESCRIPTION OF THE DRAWINGS

Other objects, features and advantages of the present invention will become more apparent from the following description taken in conjunction with the

accompanying drawings, in which:

Fig. 1 is a diagram depicting the video data structure according to MPEG-4;

Fig. 2 is a diagram showing a concrete example of VOP;

Fig. 3 is a block diagram illustrating a VOP encoder part according to a first embodiment of the present invention;

Fig. 4 is a block diagram illustrating an example of the configuration of a header multiplexing part of the VOP encoder part according to the first embodiment;

Fig. 5 is a diagram for explaining a modulo time base and a VOP time increment;

Fig. 6 is a block diagram illustrating another example of the configuration of the header multiplexing part of the VOP encoder part according to first embodiment;

Fig. 7 is a block diagram depicting a VOP encoder part according to a second embodiment of the present invention;

Fig. 8 is a block diagram depicting an example of the configuration of the header multiplexing part of the VOP encoder part according to the second embodiment;

Fig. 9 is a diagram showing an example of a bit stream;

Fig. 10 is block diagram showing an example of the configuration of a VOP header multiplexing part of the header multiplexing part in the second embodiment;

Fig. 11 is a block diagram depicting the internal configuration of a VOP decoder part according to a third embodiment of the present invention;

Fig. 12 is a block diagram depicting an example of the configuration of a header analysis part of the VOP decoder part according to the third embodiment;

Fig. 13 is a block diagram depicting a system for synthesizing a plurality of objects according to the third embodiment;

Fig. 14 is a block diagram illustrating an example of the configuration of a header analysis part of a VOP decoder part according to a fourth embodiment of the present invention;

Fig. 15 is a block diagram illustrating another example of the configuration of the header analysis part of the VOP decoder part according to the fourth embodiment;

Fig. 16 is a block diagram showing the internal configuration of a VOP

decoder part according to a fifth embodiment of the present invention;
Fig. 17 is a block diagram illustrating an example of the configuration of a header analysis part of the VOP decoder part according to the fifth embodiment;
Fig. 18 is a block diagram illustrating an example of the configuration of a VOP header analysis part of the VOP decoder part according to the fifth embodiment;
Fig. 19 is a block diagram showing an example of the configuration of a header analysis part of a VOP decoder part according to a sixth embodiment of the present invention;
Fig. 20 is a block diagram showing an example of the configuration of a VOP header analysis part of the VOP decoder part according to the sixth embodiment;
Fig. 21 is a block diagram illustrating an example of the configuration of a header multiplexing part of a VOP encoder part according to a seventh embodiment of the present invention;
Fig. 22 is a block diagram illustrating another example of the configuration of the header multiplexing part of the VOP encoder part according to the seventh embodiment;
Fig. 23 is a block diagram depicting an example of the internal configuration of a VOP decoder part according to an eighth embodiment of the present invention;
Fig. 24 is a block diagram depicting an example of the configuration of a header analysis part of the VOP decoder part according to the eighth embodiment;
Fig. 25 is a block diagram illustrating a system for synthesizing a plurality of objects according to the eighth embodiment;
Fig. 26 is a block diagram depicting another example of the configuration of the header analysis part of the VOP decoder part according to the eighth embodiment;
Fig. 27 is a block diagram depicting another example of the internal configuration of the VOP decoder part according to the eighth embodiment;
Fig. 28 is block diagram showing an example of the configuration of a header multiplexing part of a VOP encoder part according to a ninth embodiment of the present invention;
Fig. 29 is a block diagram illustrating an example of the configuration of a

header analysis part of a VOP decoder part according to a tenth embodiment of the present invention;

Fig. 30 is a block diagram illustrating an example of the configuration of a header multiplexing part of a VOP encoder part according to an eleventh embodiment of the present invention; and

Fig. 31 is a block diagram illustrating an example of a header analysis part of a VOP decoder part according to a twelfth embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

To facilitate a better understanding of the present invention, a description will be given, with reference to the accompanying drawings, of the best mode for carrying out the invention.

EMBODIMENT 1

An image encoding device according to a first embodiment (Embodiment 1) of the present invention will be described as being applied to the MPEG-4 video encoding system disclosed in ISO/IEC JTC11 SC29/WG11/N1796. The VOP encoder of this embodiment is provided with means for encoding an image on the basis of object display speed information and means for multiplexing the object display speed information onto an image-encoded bit stream by adding the information for each object.

The MPEG-4 system is a system that regards a moving picture sequence as a set of moving picture objects taking arbitrary forms temporally and spatially and performs encoding and decoding for each moving picture object. In Fig. 1 there is depicted the video data structure in MPEG-4. In MPEG-4: the moving picture object containing the time axis is called a video object [Video Object (hereinafter referred to as VO)]; a component of the VO is called a video object layer [Video Object Layer (hereinafter referred to as VOL)]; a component of the VOL is called a group of video object planes [Group of Video Object Planes (hereinafter referred to as GOP)]; and image data which represents the state of the GOP at each time and forms the basic unit for encoding is called a video object plane [Video Object Plane (hereinafter referred to as VOP)]. The VO corresponds, for example, to

each speaker or the background in a video conference scene. The VOL forms the basic unit having inherent temporal and spatial resolutions of the speaker or background. And the VOP is image data of such a VOL at each time (corresponding to a frame). The GOV is a data structure that forms the basic unit for editing a plurality of VOLs or random access thereto; this data structure need not always be used for encoding.

A concrete example of VOP is shown in Fig. 2. In Fig. 2 there are depicted two VOPs (VOP1 indicating a man and VOP2 a picture on the wall). Each VOP is composed of texture data representing the color gradation level and shape data representing the shape of the VOP. The texture data is composed of a luminance signal of 8 bits per pixel and a color difference signal (of a size subsampled to 1/2 that of the luminance signal in the horizontal and vertical directions). The shape data is the same binary matrix data as the image size of the luminance signal which sets the inside and outside of the VOP at 0 and 1, respectively.

In the VOP-based moving picture representation a conventional frame image is obtained by arranging a plurality of VOPs in the frame. When the moving picture sequence contains only one VO, each VOP is synonymous with the frame.

In this instance, no shape data exists and only the texture data is encoded. A description will be given below of the image encoding device of Embodiment 1. This is based on an MPEG-4 video encoder, which will hereinafter be referred to as a VOP encoder since it performs encoding for each VOP. The operation of the existing VOP encoder is disclosed, for example, in ISO/IEC JTC1/SC29/WG11/N1796, and hence it will not be described here, but instead a description will be given of a VOP encoder that contains constituents of Embodiment 1.

Fig. 3 depicts in block form an example of the configuration of the VOP encoder according to Embodiment 1. Reference numeral 110 denotes a VOP-to-be-encoded determination part, 111 a shape encoding part, 113 a motion estimation part, 115 a motion compensation part, 118 a texture encoding part, 122 a memory, 124 a header multiplexing part, 126 a video signal multiplexing part, 128 a subtractor, and 129 an adder.

Next, the operation of the VOP encoder will be described. Based on a VOP rate 7 that is set externally or in accordance with the encoding condition, the VOP-to-be-encoded determination part 110 determines the VOP to be

encoded in the input object images, and outputs the VOP to be encoded to the shape encoding part 111, the motion estimation part 113 and the subtractor 128. The VOP rate 7 mentioned herein refers to a value that represents how many VOPs in each VOL or GOV are to be displayed per second. And the VOP rate information also mentioned herein is a code word corresponding to the VOP rate 7, and it is equivalent to what is called the display speed information in the present invention.

The operation of the VOP-to-be-encoded determination part 110 will be described concretely. When the number of input object images is 30/sec and the VOP rate 7 is 15/sec, the VOP-to-be-encoded determination part 110 judges that alternate ones of the VOPs contained in the input object images are to be encoded, and outputs every other VOPs to be encoded.

The VOPs specified by the VOP-to-be-encoded determination part 110 as those to be encoded have their shape data encoded for each area with 16 by 16 pixels, which is commonly called an alpha block, and have their texture data encoded for each area with 16 by 16 pixels which is called a macro block.

The shape encoding part 111 encodes the alpha block input thereto and outputs encoded shape information 112 and locally decoded shape information 109. The encoded shape information 112 is fed to the video signal multiplexing part 126. The locally decoded shape information 109 is input into the motion estimation part 113, the motion compensation part 115 and the texture encoding part 118. The motion estimation part 113 reads out reference data 123a from the memory 122 and performs block matching for each macro block to obtain motion information 114. At the same time, the motion estimation part 113 gets motion information by block matching of only the objects in the macro block on the basis of the locally decoded shape information 109.

The motion compensation part 115 reads out of the memory 122 reference data 123b on the position indicated by the motion information 114 and generates a predictive image 116 based on the locally decoded shape information 109. The predictive image 116 created in the motion estimation part 115 is provided to the subtractor 128 and the adder 129.

The subtractor 128 calculates the difference between the predictive image 116 and the input macro block to provide a prediction-error image 117, which is fed to the texture encoding part 118.

The texture encoding part 118 encodes the prediction-error image 117 by a predetermined method prescribed by MPEG-4 to obtain encoded texture information 119 and a locally decoded prediction-error image 120. In this instance, only the objects contained in the block are encoded based on the locally decoded shape information 109. The encoded texture information 119 is provided to the video signal multiplexing part 126. The locally decoded prediction-error image 120 is fed to the adder 129.

The adder 129 adds the predictive image 116 and the locally decoded prediction-error image 120 to create a decoded image 121, which is written in the memory 122.

In the header multiplexing part 124 respective pieces of header information are multiplexed to generate a bit stream 125, which is input into the video signal multiplexing part 126.

The video signal multiplexing part 126 multiplexes the encoded shape information 112, the motion information 114 and the encoded texture information 119 onto the bit stream 125, and outputs an encoded VOP bit stream.

Fig. 4 is a block diagram depicting the configuration of the header multiplexing part shown in Fig. 3. In Fig. 4, reference numeral 1 denotes a VO header multiplexing part, 2 a VOL header multiplexing part, 3 a GOV header multiplexing selection part, 4 a GOV header multiplexing part, 5 a VOP header multiplexing part, 6 GOV multiplexing information, and 7 the VOP rate.

Next, the operation of the header multiplexing part will be described. The VO header multiplexing part 1 multiplexes VOP header information to create a bit stream, and outputs it to the VOL header multiplexing part 2. The VOL header multiplexing part 2 multiplexes VOL header information onto the input bit stream, and outputs the multiplexed bit stream to the GOV header multiplexing selection part 3.

The GOV header multiplexing selection part 3 determines the destination of the bit stream fed from the VOL header multiplexing part 2 based on the GOV multiplexing information 6 indicating whether to perform the multiplexing of the GOV header. When the GOV multiplexing information 6 indicates that no multiplexing of the GOV header takes place, the bit stream is output to the VOP header multiplexing part 5. When the GOV multiplexing information 6 indicates that the multiplexing of the GOV

header is performed, the bit stream is provided to the GOV header multiplexing part 4.

Table 1 shows, by way of example, four values of the VOP rate 7. When the VOP rate is 30/sec, VOP rate information "01" is multiplexed. When the VOP to be encoded is the same as the VOP encoded immediately previously, VOP information "00" is multiplexed but the subsequent VOP header information and VOP data information are not multiplexed. When the VOP rate is variable, VOP rate information "11" is multiplexed. That is, the VOP rate information indicates whether the VOP rate is fixed or variable, and represents the value of the rate when it is fixed.

A VOP start code multiplexing part 8 in the VOP header multiplexing part 5 outputs to a modulo time base multiplexing part 9 and a VOP time increment multiplexing part 10 a bit stream obtained by multiplexing a VOP start code onto the input bit stream.

The modulo time base 13 mentioned herein is information that represents what number of seconds will pass until the VOP concerned is displayed after a certain reference time as depicted in Fig. 5. The VOP time increment 14 is information that is used to fine-adjust the display time defined by the modulo time base with an accuracy of 1/1000th of a second also as shown in Fig. 5. That, is, MPEG-4 permits defining the VOP display time with a precision of 1/1000th of a second.

A management time generating part 12 in the VOP header multiplexing part 5 generates the modulo time base 13 and the VOP time increment 14 based on the VOP rate 7, and outputs the former to the modulo time base multiplexing part 9 and the latter to the VOP time increment multiplexing part 10. When the VOP rate 7 indicates a variable rate, the modulo time base 13 and the VOP time increment 14 are set independently of the VOP rate 7.

The modulo time base multiplexing part 9 multiplexes the modulo time base 13 onto the bit stream provided from the VOP start code multiplexing part, and outputs the multiplexed bit stream to the VOP time increment multiplexing part 10. The VOP time increment multiplexing part 10 multiplexes the VOP time increment 14 fed thereto from the management time generating part 12 onto the bit stream fed from the modulo time base multiplexing part 9, and outputs the multiplexed bit stream to a video information header multiplexing part 11. The video information header

multiplexing part 11 multiplexes a video information header onto the bit stream provided thereto from the VOP time increment multiplexing part 10, and outputs the multiplexed bit stream to the video signal multiplexing part 126.

As described above, according to Embodiment 1, since the VOP rate information is multiplexed onto the GOV header, a bit stream can be created which enables the decoder side to determine whether or not to require the decoding of the VOP concerned, or to synthesize a plurality of objects, simply by analyzing only the VOP start code of each VOP header.

It is also possible to define the VOP rate information for each VOL and perform encoding and multiplexing of the VOP rate 7 as shown in Fig. 6. In this instance, the VOP rate 7 is determined for each VOL and is multiplexed in the VOL header multiplexing part 2. The VOP rate 7 is used to determine the modulo time base 13 and the VOP time increment 14. As described above, the image encoding device of Embodiment 1, which encodes images object by object, is provided with: encoding means for encoding the images on the basis of predetermined display speed information; and multiplexing means for multiplexing the predetermined display speed information onto the image signals encoded by the encoding means and for outputting the multiplexed signals.

Furthermore, the multiplexing means may also be modified to multiplex the display speed information on an object-by-object basis.

EMBODIMENT 2

A second embodiment (Embodiment 2) of the present invention concerns a modified form of the VOP encoder described above in Embodiment 1. The VOP encoder of Embodiment 2 is provided with means for encoding a 1-bit VOP rate flag that indicates as the display speed information whether the object display speed is fixed or variable and the VOP rate information that indicates the value of the object display speed, and for multiplexing them into the bit stream.

When the VOP rate flag indicates a variable speed, the VOP rate corresponds to "variable" in Table 1 referred to previously in respect of Embodiment 1. When the VOP rate flag indicates a fixed speed, the VOP rate corresponds to 30/sec or 15/sec in Table 1.

Fig. 7 shows an example of the configuration of the VOP encoder according to Embodiment 2. Reference numeral 1000 denotes a header multiplexing part, 1001 the VOP rate flag, and 1026 the VOP rate. Since the VOP encoder according to this embodiment differs from the VOP encoder of Embodiment 1 only in the configuration and operation of the header multiplexing part 1000 that is the counterpart 124 of the latter, a description will be given in this respect alone.

Fig. 8 is a block diagram depicting the configuration of the header multiplexing part 1000 of the VOP encoder according to Embodiment 2. In Fig. 8, reference numeral 1002 denotes a VOL header multiplexing part, and 1003 a VOP header multiplexing part.

Next, the operation of this embodiment will be described.

The VOP header multiplexing part 1 creates a bit stream by multiplexing VO header information, and outputs the thus created bit stream to the VOL header multiplexing part 1002. The VOL header multiplexing part 1002 multiplexes VOL header information onto the input bit stream, and outputs the multiplexed bit stream to the GOV header multiplexing selection part 3. In this case, the VOL rate and the VOP rate flag are also multiplexed.

Table 3 shows examples of multiplexing of the VOP rate 1026. In this instance, when the VOP rate 1026 is 2/sec, "000" is multiplexed as the VOP rate information. When the VOP rate is 5/sec, "001" is multiplexed. When the VOP rate is 25/sec, "001" is multiplexed. When the VOP rate is 30/sec, "011" is multiplexed. For other VOP rates (for example, when the VOP rate is 10/sec), "100" is multiplexed. Incidentally, a decision as to whether to multiplex the VOP rate information is made independently of the VOP flag value described later on. The multiplexing of the VOP rate may also be done as exemplified in Table 4. In this case, when all VOPs are related to exactly the same image in the VOL, the image is regarded as a still picture and "010" is multiplexed as the VOP rate information.

Depending on whether the VOP rate flag indicates a fixed or variable speed, "1" or "0" is multiplexed. Fig. 9 depicts an example of the bit stream provided from the VOL header multiplexing part 1002.

The GOV header multiplexing selection part 3 determines the destination of the bit stream fed thereto from the VOL header multiplexing part 102 based on the GOV multiplexing information 6 indicating whether to multiplex the GOV header. When the GOV multiplexing information 6 does not

indicates the multiplexing of the GOV header, the bit stream is provided to the VOP header multiplexing part 1003. When the GOV multiplexing information 6 indicates the multiplexing of the GOV header, the bit stream is provided to the GOV header multiplexing part 4.

The GOV header multiplexing part 4 multiplexes the GOV header information onto the input bit stream, and outputs the multiplexed bit stream to the VOP header multiplexing part 1003. Fig. 10 illustrates the VOP header multiplexing part 1003 in detail. Reference numeral 1004 denotes a management time generating part.

Next, the operation of the GOV header multiplexing part 4 will be described. The management time generating part 1004 generates a modulo time base and a VOP time increment based on the VOP rate 1026 when the input VOP rate flag 1001 indicates a fixed speed, and based on a timer contained in the VOP encoder when the input VOP rate flag 1001 indicates a variable speed. The modulo time base and the VOP time increment thus created are provided to the modulo time base multiplexing part 9 and the VOP time increment multiplexing part 10, respectively.

The VOP time increment multiplexing part 10 multiplexes the VOP time increment onto the input bit stream, and outputs the multiplexed bit stream to the video information header multiplexing part 11. The video information header multiplexing part 11 multiplexes the video information header onto the bit stream provided thereto from the VOP time increment multiplexing part 10, and outputs the multiplexed bit stream to the video signal multiplexing part 126.

As described above, according to Embodiment 2, since the VOP rate flag and the VOP rate information are multiplexed onto the VOL layer, a bit stream can be created which enables the user to specify his desired VOP in a moment through utilization of the VOP rate flag and the VOP rate on the decoder side--this allows him to determine whether or not to require the decoding of the VOP concerned, or to synthesize a plurality of objects, simply by analyzing only the VOP start code of the corresponding VOP header.

Incidentally, since it is possible to distinguish between variable and fixed speeds even if only the VOP rate flag is multiplexed, the VOP desired to decode can be decoded.

As described above, the image encoding device according to Embodiment 2,

which encodes images on an objectwise basis, is provided with: means for encoding the flag indicating whether the object display speed is fixed or variable; multiplexing means for multiplexing the flag onto the encoded image signal encoded by the encoding means and for outputting the multiplexed signal; encoding means for encoding the images on the basis of predetermined display speed information; and multiplexing means for multiplexing the predetermined display speed information onto the image signals encoded by the encoding means and for outputting the multiplexed signals.

EMBODIMENT 3

A third embodiment (Embodiment 3) of the present invention is directed to an image decoding device for decoding from an encoded bit stream the VOP rate information mentioned previously in connection with Embodiment 1, that is, an MPEG-4 video decoder (hereinafter referred to as a VOP decoder). The image decoding device of this embodiment is applicable to a system which employs such decoding devices in one-to-one correspondence to a plurality of objects and synthesizes decoded objects to reconstruct a pictorial image.

A description will be given first of the configuration and operation of the image decoding device (VOP decoder) of Embodiment 3. Since the operation of the existing VOP decoder is disclosed, for example, in ISO/IEC JTC1/SC29/WG11/N1796, the VOP decoder of a novel configuration according to this embodiment will be described without referring to the existing VOP decoder itself. The VOP decoder of this embodiment is one that is able to decode the encoded bit stream generated by the VOP encoder described previously with reference to Embodiment 1.

Fig. 11 depicts an example of the internal configuration of the VOP decoder according to Embodiment 3. The VOP decoder is supplied with compressed-encoded data composed of texture data and shape data as described previously with reference to Embodiment 1 and shown in Fig. 2, and decodes the individual pieces of data. In Fig. 11, reference numeral 150 denotes encoded VOP bit stream, 151 a header analysis part, 152 a bit stream with the header information analyzed, 153 a video signal analysis part, 154 encoded shape data, 155 a shape decoding part, 156 decoded shape

data, 157 encoded texture data, 158 motion information, 159 a motion compensation part, 160 predictive texture data, 161 a texture decoding part, 162 decoded texture data, 164 a memory, and 165 reference data.

Referring to Fig. 11, the operation of the decoder will be described in detail. The encoded VOP bit stream 150 is input into the header analysis part 151, wherein the header information is analyzed following a predetermined syntax. The bit stream having the header information analyzed in the header analysis part 151 is fed into the video signal analysis part 153, wherein it is analyzed into the encoded shape data 154, the encoded texture data 157 and the motion information 158. The shape decoding part 155 decodes the encoded shape data input thereinto, and outputs the decoded shape data 156.

The motion compensation part 159 generates the predictive texture data 160 from the reference data 165 read out of the memory 164 and the motion information 158 provided from the video signal analysis part 153, and provides the predictive texture data 160 to the texture decoding part 161. Based on the encoded texture data 157 and the predictive texture data 160, the texture decoding part 161 reconstructs image data by the method prescribed in MPEG-4, generating the decoded texture data 162. The decoded texture data 162 is written in the memory 164 so that it is used afterward for VOP decoding.

Fig. 12 depicts the internal configuration of the header analysis part 151 characteristic of Embodiment 3. In Fig. 12, reference numeral 51 denotes a start code analysis part, 52 a VO header analysis part, 53 a VOL header analysis part, 54 a GOV header analysis part, 58 VOP rate information, and 55 a VOP header analysis part. The header analysis part 151 in Embodiment 3 is characterized in that the GOV header analysis part 54 decodes the VOP rate information of VOP contained in the GOV concerned from the bit stream and provides it to the outside. A description will be given later of how to use the VOP rate information 58.

The start code analysis part 51 analyzes the start code contained in the encoded VOP bit stream 150 input thereinto. The start code analysis part 51 outputs the bit stream to the VO header analysis part when the analyzed start code is indicative of VOL, to the VOL header analysis part 53 when the start code is indicative of VOL, to the GOV header analysis part 54 when the start code is indicative of GOV, and to the VOP header analysis part 55

when the start code is indicative of VOP. Incidentally, upon completion of the analysis in the VOP header analysis part 55, the bit stream is output to the video signal analysis part 153.

The VO header analysis part 52 analyzes VO header information from the input bit stream, and outputs the analyzed bit stream to the start code analysis part 51. The VOL header analysis part 53 analyzes VOL header information from the input bit stream, and outputs the bit stream to the start code analysis part 51. The GOV header analysis part 54 analyzes GOV header information from the input bit stream, and outputs the bit stream to the start code analysis part 51. At this time, the VOP rate information 58 contained in the GOV header information is decoded and output. The VOP header analysis part 55 analyzes VOP header information from the input bit stream, and outputs the bit stream via the start code analysis part 51 to the video signal analysis part 153.

With the VOP decoder of the above configuration and operation, it is possible to output, for each GOV, the VOP rate information of VOPs contained therein. Fig. 13 illustrates a system that uses this information to synthesize a plurality of objects. In Fig. 13, reference numeral 200 denotes an encoded VOP bit stream a, 201 an encoded VOP bit stream b, 202 an encoded VOP bit stream c, 203a a VOP decoder for decoding the encoded VOP bit stream a200, 203b a VOP decoder for decoding the encoded VOP bit stream b201, 203c a VOP decoder for decoding the encoded bit stream c202, 204 a decoded object image a, 205 a decoded object image b, 206 a decoded object image c, 207 VOP rate information a, 208 VOP rate information b, 209 VOP rate information c, 210 a composition part, and 211 a decoded pictorial image. The decoded object image herein mentioned refers to an image that is obtained by combining the decoded shape data 154 and the corresponding decoded texture data 162 for each of VOPs and then integrating such combined pieces of data for each group of VOPs (for example, GOV or VOL).

The encoded VOP bit streams a200 to c202 are decoded by the VOP decoder 203a to 203c corresponding thereto, respectively, by which the decoded VOP images a204 to c206 are generated. At this time, the VOP decoder decodes the corresponding VOP rate information a207 to c209, and output them to the composition part 210. Based on the VOP rate information a207 to c209, the composition part 210 determines the times of the frames of the pictorial

image 211 in which to synthesize the decoded VOP images, and maps them into the frames corresponding to the determined times. Let it be assumed, for example, that the decoded image 211 is displayed at a rate of 30 video object planes per sec (which corresponds to an ordinary TV signal display speed). Furthermore, assume the following situations.

- * The decoded VOP image a204 is displayed at a rate of 5/sec (that is, the VOP rate information a207 indicates the 5/sec rate).

- * The decoded VOP image b205 is displayed at a rate of 10/sec (that is, the VOP rate information indicates the 10/sec rate).

- * The decoded VOP image c206 is displayed at a rate of 15/sec (that is, the VOP rate information c209 indicates the 15/sec rate).

In this instance, the decoded VOP images a204 to c206 are all mapped into the first image frame at each second in the decoded image 211; the decoded VOP image a204 is mapped into every five image frames including the first at each second; the decoded VOP image b205 is mapped into every 10 image frames including the first at each second; and the decoded VOP image c206 is mapped into every 15 image frames including the first at each second. By this, it is possible to display a pictorial image with a plurality of objects synthesized in the image frames in accordance with their display speeds.

With the use of VOP decoders each of which decodes the encoded bit stream having the VOP rate information encoded in the GOV layer as described above, it is feasible to implement a simple-structured system which synthesizes a plurality of object into a reconstructed image.

The VOP rate information may also be encoded for each VOL at the image encoding device side. In this case, it is possible, at the image decoding device side, to decode the VOP rate encoded for each VOL and synthesize a plurality of objects for each VOL as described above.

While in the above the VOP decoders have been described to be used in a system for synthesizing a plurality of objects, it is also feasible to use only one VOP decoder for a system that decodes only one object to reconstruct an image.

As described above, according to Embodiment 3, the image decoding device which decodes the bit stream encoded from an image on an object-by-object basis is provided with: display speed information decoding means for decoding display speed information from the encoded bit stream; and control means for controlling the reconstruction of the image encoded on the

object-by-object basis through utilization of the display speed information decoded by the display speed information decoding means.

In Embodiment 3 the display speed information decoding means has been described to decode the display speed information object by object.

EMBODIMENT 4

A fourth embodiment (Embodiment 4) of the present invention is directed to a modified form of the VOP decoder of Embodiment 3. The VOP decoder according to this embodiment has a function of specifying the VOP to be decoded on the basis of the value of the VOP rate that the decoder assumes. Since the VOP decoder of Embodiment 4 differs from that of Embodiment 3 only in the configuration and operation of the header analysis part 151, a description will be given only in this respect.

Fig. 14 is a block diagram illustrating the configuration of the header analysis part of the VOP decoder according to Embodiment 4, in which the VOP rate at the encoder side and the VOP rate at the decoder side do not match. In Fig. 14, reference numeral 59 denotes a VOP-to-be-decoded selection part, which compares a VOP rate from the GOV header analysis part 54 and a VOP rate assumed at the decoder side, and outputs VOP select information 62. And the VOP header analysis part 55 has a counter 60 in addition to a time management information header analysis part 56 and a video information header analysis part 57.

Next, the operation of this embodiment will be described. The VOP-to-be-decoded selection part 59 outputs to the counter part 60 of the VOP header analysis part 55 the VOP select information that indicates information about the VOP to be decoded according to the result of comparison between the VOP rate 58 analyzed in the GOV header analysis part 54 and the VOP rate 61 assumed at the decoder side. The counter part 60 uses the VOP select information 62 to determine whether to decode the VOP header information that follows the VOP start code contained in the input bit stream.

More specifically, when the VOP rate 58 analyzed in the GOV header analysis part 55 is 30/sec and the VOP rate assumed at the decoder side is 15/sec, the VOP select information 62 indicating that every other VOPs are analyzed is provided to the counter part 60 in the VOP header analysis part

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55. The counter part 60 first counts every VOP header input therein by a counter 60a.

Then, based on the count value input therein from the counter 60a and the VOP rate select information 62 from the VOP-to-be-decoded selection part 59, decision means 60b decides whether the input VOP needs to be analyzed. When the input VOP is decided to be analyzed, the input bit stream is output to the time management information header analysis part 56. When the input VOP is decided not to be analyzed, the input bit stream is fed to the start code analysis part 51.

To be more specific, when the VOP rate select information 62 is information that one VOP needs to be analyzed for every three VOPs, the decision means 60b judges that the VOP must be analyzed for which the count value from the counter 60a can be divided by 3 without a remainder, and that the VOP need not be analyzed for which the count value from the counter 60a is divided by 3, with a remainder of 1 or 2.

While the VOP decoder of Embodiment 4 has been described to be adapted for use in the case where the VOP rate information is contained in the GOV header, the VOP rate information may also be contained in the VOL header as described previously with reference to Embodiment 2. In such an instance, the VOL header analysis part 300 needs only to be equipped with the function of decoding the VOP rate information 58.

Moreover, the VOP decoder of this embodiment can be used not only in a system which synthesizes a plurality of objects but also in a system which decodes and reconstructs only one object.

As described above, the decoder according to Embodiment 4 has, as control means, decoding time specifying means for specifying the time when to decode an object on the basis of the object display information decoded by the display speed information decoding means and the object display speed information preset in the decoding device; and decoding means for decoding the object at the decoding time specified by the decoding time specifying means.

EMBODIMENT 5

A fifth embodiment (Embodiment 5) of the present invention is directed to a modified form of the VOP decoder Embodiment 3 or 4. The VOP decoder

according to this embodiment is equipped with a function of specifying a VOP to be decoded on the basis of: the VOP rate flag indicating whether the object display speed is fixed or variable; the VOP rate information indicating the object display speed; externally-set display control information indicating time information externally set by a user; and a time code.

Since the VOP decoder of Embodiment 5 differs, as shown in Fig. 16, from the VOP decoder of Embodiment 3 only in the configuration and operation of a header analysis part 1005 corresponding to the header analysis part 151 of the VOP decoder of the latter, a description will be given in this respect alone.

Fig. 17 is a block diagram illustrating the configuration of the header analysis part 1005 of the VOP decoder according to this embodiment. Reference numeral 1006 denotes a VOL header analysis part, 1007 a GOV header analysis part, 1008 a VOP header analysis part, 1009 an externally-set display control information, 1010 a VOP rate flag, and 1011 a time code. Incidentally, the externally-set display control information 1009 may be information indicating absolute time or VOP select information indicating the number of VOPs from which one VOP to be decoded is selected.

Next, the operation of this embodiment will be described. The start code analysis part 51 analyzes the start code contained in the input encoded VOP bit stream. The start code analysis part outputs the bit stream to the VO header analysis part 52 when the analyzed start code indicates VO, to the VOL header analysis part 1006 when the start code indicates VOL, to the GOV header analysis part 1007 when the start code indicates GOV, and to the VOP header analysis part 1008 when the start code indicates VOP. The bit stream is provided to the video signal analysis part 153 after completion of the analysis in the VOP header analysis part 1008.

Next, the VO header analysis part 52 analyzes the VO header, the VOP rate information 58 and the VOP rate flag 1011 contained in the input bit stream, and outputs the analyzed bit stream to the start code analysis part 51 and, at the same time, outputs the VOP rate information 58 to the composition part 210 and the VOP header analysis part 1008 and the VOP rate flag 1010 to the VOP header analysis part 1008.

The GOV header analysis 1007 analyzes the GOV header contained in the input bit stream, and outputs the analyzed bit stream to the start code analysis part 51 and, at the same time, outputs the time code 1011

contained in the analyzed GOV header to the VOP header analysis part 1008.

Fig. 18 is a block diagram illustrating the VOP header analysis part 1008 in detail. Reference numeral 1012 denotes a VOP-to-be-decoded determining part (1), which has a counter part 1012a and decision means 1012b. Reference numeral 1013 denotes a modulo time base analysis part, 1014 a VOP time increment analysis part, 1015 a VOP-to-be-decoded determining part (2), and 1016 a VOP-to-be-decoded determining method select part.

Next, the operation of the VOP header analysis part 1008 will be described. The VOP-to-be-decoded determining method select part 1016 uses the VOP rate flag 1010 to determine the destination of the input bit stream. When the VOP rate flag 1010 indicates a fixed speed, the VOP-to-be-decoded determining part (1) 1012 is selected as the destination of the bit stream, and when the VOP rate flag 1010 indicates a variable speed, the modulo time base analysis part 1013 is selected.

A description will be given first of the case where the VOP rate flag 1010 indicates a fixed speed. The counter part 1012a in the VOP-to-be-decoded determining part (1) 1012 increments its count value upon each input of the bit stream into the VOP header analysis part 1006 when the VOP start code is detected in the start code analysis part 51, and outputs the count value and the bit stream to the decision means 1012b.

Then the decision means 1012b decides whether the VOP to be decoded needs to be decoded. The operation of the decision means 1012b will be described below in respect of first and second cases where the externally-set display control information 1009 is provided as absolute time and where the externally-set display control information 1009 is provided as VOP select information.

(First Case)

Based on the count value and the VOP rate information fed thereto from the counter part 1012a and the time code 1011, the decision means 1012b calculates the absolute time that the VOP candidate for decoding has. For example, in the case where the count value is 4, the VOP rate is 2/sec and the absolute time is 0h10m0sec0msec, the absolute of the VOP candidate for decoding has is calculated to be 0h10m02sec0msec. If the thus calculated absolute time of the VOP candidate for decoding and the externally-set display control information 1009 are equal to each other, the VOP is decided

to be decoded.

On the other hand, when they are not equal, the absolute time of the next VOP candidate for decoding is calculated. This is intended to make comparison between the absolute time of the next VOP candidate for decoding and the absolute time of the current VOP candidate so as to ensure decoding of the VOP of the absolute value closer to the externally-set display control information 1009. The absolute time of the next VOP candidate for decoding is calculated from the already calculated absolute time of the current VOP candidate and the VOP rate information 58. When this calculated value is smaller or equal to the externally-set display control information 1009, the next VOP candidate is decided to be decoded, and the current VOP candidate is not decoded. When the calculated value exceeds the externally-set display control information 1009, any of the following methods may be chosen.

- * Decode the current VOP candidate;

- * Decode the next VOP candidate (=not decode the current VOP candidate for decoding);

- * Decode the VOP of an absolute time which has a small difference between it and the externally-set display control information 1009, that is, which is close to the externally-set display control information 1009.

(Second Case)

This is the case where the display speed is controlled at the VOP decoder side; for example, the user is allowed to determine the display speed or designate the optimum display speed according to CPU resources.

Next, the operation in this case will be described. Let it be assume that the VOP select information indicates the necessity for decoding one for every three VOPs. In this instance, the decision means 1012b judges that decoding needs to be done when the count value fed thereto from the counter part 1012a can be divided by 3 without a remainder and that decoding need not be done when the count value from the counter part 1012a is divided by 3 with a remainder 1 or 2.

In either of the first and second cases, when it is judged that the VOP candidate for decoding must be decoded, the decision means outputs a bit stream to the modulo time base analysis part 1013, and to the start code analysis part 51 when it is judged that no decoding is necessary. The modulo time base analysis part 1013 analyzes the modulo time base, and

outputs a bit stream to the VOP time increment analysis part 1014.

The VOP time increment analysis part 1014 analyzes the VOP time increment, and outputs a bit stream to the video information header analysis part 57. The video information header analysis part 57 analyzes the video information header, and outputs a bit stream to the start code analysis part 51.

Next, a description will be given of the case where the VOP rate flag 1010 indicates a variable speed. The modulo time base analysis part 1013 analyzes the modulo time base, and outputs a bit stream to the VOP time increment analysis part 1014. The VOP time increment analysis part 1014 analyzes the VOP time increment, and outputs a bit stream to the VOP-to-be-decoded determining part (2) 1015.

Based on the modulo time base analyzed in the modulo time base analysis part 1013, the VOP time increment analyzed in the VOP time increment analysis part 1014 and the time code 1011, the VOP-to-be-decoded determining part (2) 1015 generates the absolute time of the VOP candidate for decoding. And based on the generated absolute time and the externally-set display control information 1009, it determines whether to decode the VOP candidate for decoding. When it is determined that the VOP needs to be decoded, a bit stream is output to the video information header analysis part 57, and to the start code analysis part 51 when it is judged that no decoding is necessary. The video information header analysis part 57 analyzes the video information header, and outputs a bit stream to the start code analysis part 51.

According to Embodiment 5, since the bit stream with the encoded VOP rate flag and VOP rate information is multiplexed onto the VOL layer as described above, it is possible for the user to specify his desired VOP in a moment through utilization of the VOP rate flag and the VOP rate--this allows him to determine whether the VOP concerned needs to be decoded, or to synthesize a plurality of objects, simply by analyzing only the VOP start code contained in the corresponding VOP header information.

Incidentally, when the VOPs contained in the encoded VOP bit stream input into the VOP decoder are all intra-encoded, the user can specify his desired VOP in a moment and cause it to be displayed.

As described above, the decoder according to Embodiment 5 is has control means which controls the image reconstruction by specifying the display

time of the image at each time for decoding on the basis of the display speed information when the display speed identification information decoded by the display speed information decoding means indicates a fixed speed and on the basis of display time information multiplexed for each image at each time in the case where the display speed identification information indicates a variable speed.

EMBODIMENT 6

A sixth embodiment (Embodiment 6) of the present invention is directed to a modified form of the VOP decoder described above in Embodiment 5. The VOP decoder according to this embodiment has a function of specifying the VOP to be decoded on the basis of the VOP rate flag indicating whether the object display speed is fixed or variable, the VOP rate indicating the object display speed, the externally-set display control information externally set by the user, and the time code.

Fig. 19 is a diagram depicting the header analysis part of the VOP decoder according to Embodiment 6. Since the VOP decoder of this embodiment differs from that of Embodiment 5 only in the configuration and operation of the VOL header analysis part 1006 and VOP header analysis part 1008, a description will be given in this respect alone.

A VOL header analysis part 1017 analyzes the VOL header, the VOP rate information and the VOP rate flag contained in the bit stream input thereto, and outputs the analyzed bit stream to the start code analysis part 51 and the VOP rate flag 1010 to a VOP header analysis part 1018. At the same time, it outputs the VOP rate information 58 to the VOP header analysis part 1016 when the analyzed VOP rate information indicates any fixed rate value (for example, the VOP rate indicated by VOP rate information "100" in Table 3), and the VOP rate information 58 to the VOP header analysis part 1018 and the composition part 210 when the analyzed VOP rate information indicates a particular value (for example, the VOP rates indicated by VOP rate information "000", "001", "010" and "011" in Table 3).

Fig. 20 is a diagram depicting the VOP header analysis part 1018 in detail. Reference numeral 1025 denotes a VOP-to-be-decoded determining method select part, and 1019 a VOP-to-be-decoded determining part (3), which has a

counter part 1919a, a count value decision part 1019b and decision means 1019c. Reference numeral 1020 denotes a time information holding part, 1021 a VOP rate information calculation part, 1022 a VOP rate information holding part, 1023 a modulo time base analysis part, and 1024 a VOP time increment analysis part.

Based on the VOP rate flag 1010 and the VOP rate information 58 input therinto, the VOP-to-be-decoded determining method select part 1025 selects the destination of the input bit stream. More specifically, when the VOP rate flag 1010 indicates a fixed speed and the VOP rate information 58 indicates some fixed rate value, the VOP-to-be-decoded determining part (3) 1019 is selected as the destination. When the VOP rate flag 1010 indicates a variable speed, the operation described previously in Embodiment 5 is performed, which will not be described again. When the VOP rate flag 1010 indicates a fixed speed and the VOP rate information 59 a particular value, the bit stream is output to the VOP-to-be-decoded determining part (1) 1012. In this instance, the VOP-to-be-decoded determining part (1) 1012 and the parts following it perform the same operations as described previously in Embodiment 5; so, no description will be repeated.

A description will be given below of the case where the VOP rate flag 1010 indicates a fixed speed and the VOP rate information 58 some fixed rate value.

The counter part 1019a in the VOP-to-be-decoded determining part (3) 1019 increments its count value whenever the VOP start code is detected in the start code analysis part 51 and a bit stream is fed into the VOP header analysis part 1018, and it outputs the count value and the bit stream to the count value decision part 1019b. The count value decision part 1019b outputs the bit stream and the count value to the modulo time base analysis part 1023 when the count value indicates a first or second VOP, and in the other cases, it outputs the bit stream and the count value to the decision means 1019c.

The modulo time base analysis part 1023 analyzes the modulo time base and, when the input count value indicates the first VOP, outputs the modulo time base to the time information holding part 1020 and to the VOP rate information calculation part 1021 when the input count value indicates the second VOP, while at the same time it outputs the bit stream and the count value to the VOP time increment analysis part 1024.

The VOP time increment analysis part 1024 analyzes the VOP time increment and, when the input count value indicates the first VOP, outputs the VOP time increment to the time information holding part 1020 and to the VOP rate information calculation part 1021 when the input count value indicates the second VOP, while at the same time it outputs the bit stream to the video information header analysis part 57. The video information header analysis part 57 analyzes the video information header, and outputs the bit stream to the start code analysis part 51.

The time information holding part 1020 holds the modulo time base and the VOP time increment input thereto. Upon input thereto of the modulo time base and the VOP time increment for the second VOP, the VOP rate information calculation part 1021 reads thereinto from the time information holding part 1020 the modulo time base for the first VOP and the VOP time increment similarly for the first VOP, then calculates the VOP rate information based on them, and outputs the VOP rate information to the VOP rate information holding part 1022. When the VOP time increment is expressed with a 6-bit accuracy, the VOP rate information calculation part 1021 calculates the VOP rate as described below.

In the case where the modulo time base for the first VOP is "10", the VOP time increment for the first VOP is "000000" (that is, the time information about the first VOP is 1.0 sec), the modulo time base for the second VOP is "10" and the VOP time increment for the second VOP is "100000" (that is, the time information about the second VOP is 1.5 sec), the difference between the time information about the both is 0.5 sec. This means that the VOP to be decoded exists every 0.5 sec, that is, the VOP rate is 2/sec (which corresponds to VOP rate information "1111" in Table 3).

Even when the VOP rate information 58 is not multiplexed, if only the VOP rate flag 1010 is multiplexed, it can be judged therefrom that a fixed rate is indicated, and consequently, such operation as described above can be carried out.

The VOP rate information holding part 1022 holds the VOP rate information input thereto, and outputs the VOP rate information to the composition part 210. The operation of the decision means 1019a will be described below in connection with first and second cases where the externally-set display control information 1009 is provided as absolute time and where the externally-set display control information is provided as the

VOP rate.

(First Case)

Based on the count value fed thereto from the count value decision part 1019b and the VOP rate information provided from the VOP rate information holding part 1022, the decision means 1019c calculates the absolute time that the VOP candidate for decoding has. When the thus calculated absolute time of the VOP candidate for decoding and the externally-set display control information 1009 are equal to each other, it is judged that decoding needs to be done.

On the other hand, when they are not equal, the absolute time of the next VOP candidate for decoding is calculated. This is intended to make comparison between the absolute time of the next VOP candidate for decoding and the absolute time of the current VOP candidate for decoding so as to ensure decoding the VOP of the absolute value closer to the externally-set display control information 1009. The absolute time of the next VOP candidate for decoding is calculated from the already calculated absolute time of the current VOP candidate and the VOP rate information 58. When this calculated value is smaller or equal to the externally-set display control information 1009, the next VOP candidate for decoding is decided to be decoded, and the current VOP candidate for decoding is not decoded. When the calculated value exceeds the externally-set display control information 1009, any of the following methods may be chosen.

- * Decode the current VOP candidate for decoding;
- * Decode the next VOP candidate for decoding (=not decode the current VOP candidate for decoding);
- * Decode the VOP of an absolute time which has a small difference between it and the externally-set display control information 1009, that is, which is close to the externally-set display control information 1009.

(Second Case)

When the VOP rate derived from the externally-set display control information 1009 is 2/sec and the VOP rate indicated by the VOP rate information fed from the VOP rate information holding part 1022 is 4/sec, the VOP information indicating the number of VOPs from which one VOP is selected for decoding becomes information that every other VOPs are to be decoded. In this instance, the decision means 1019c judges that the VOP for which the count value input thereto from the count value decision part

1019b can be divided by 2 without a remainder is to be decoded, and that the VOP for which the count value from the count value decision part 1019b is divided by 2 but with a remainder of 1 is not to be decoded.

In both of the first and second cases, when the VOP candidate for decoding is decided to be decoded, the bit stream is output to the modulo time base analysis part 1013, and when it is decided that no decoding is needed, the input bit stream is output to the start code analysis part 51. The modulo time base analysis part 1013 analyzes the modulo time base, and outputs the bit stream to the VOP time increment analysis part 1014. The VOP time increment analysis part 1014 analyzes the VOP time increment, and outputs the bit stream to the video information header analysis part 57. The video information header analysis part 57 analyzes the video information header, and outputs the bit stream to the start code analysis part 51.

As described above, according to Embodiment 6, the bit stream with the encoded VOP rate flag and VOP rate information is multiplexed onto the VOL layer, and the VOP rate information is calculated from the absolute times of the first and second VOPs when the VOP rate flag indicates a fixed speed. Hence, it is possible for the user to specify his desired VOP in a moment through utilization of the VOP rate flag and the VOP rate--this allows him to determine whether the VOP concerned needs to be decoded, or to synthesize a plurality of objects with ease, simply by analyzing only the VOP start code contained in the corresponding VOP header information with respect to an arbitrary fixed VOP rate.

Incidentally, when the VOPs contained in the encoded VOP bit stream input into the VOP decoder are all intra-encoded, the user can specify his desired VOP in a moment and cause it to be displayed.

As described above, the decoder according to Embodiment 6 has control means which controls image reconstruction by specifying the display time of the image at each time for decoding on the basis of the display speed information multiplexed for each image at each time in the case where the display speed identification information decoded by the display speed information decoding means indicates a fixed speed and the fixed speed is a value not represented by the display speed information.

EMBODIMENT 7

A seventh embodiment (Embodiment 7) of the present invention is directed to a modified form of the VOP encoder described previously in Embodiment 1. The VOP encoder of this embodiment has a function of adding, for each VOL, the time code that defines the absolute display time of each VOP contained in the VOL concerned.

The time code mentioned herein is time information disclosed in IEC standard publication 461 for "time and control codes for video tape recorders", which is information that defines the display time of an image at each time forming a moving picture (a frame in MPEG-2 and a VOP in MPEG-4) with an accuracy of hour/minute/second. For example, in the case of performing video editing on a frame-by-frame basis by commercial video editor, the addition of this information to each frame makes it possible to access a desired frame simply by designating the value of the time code. Since the VOP encoder of this embodiment differs from the encoder of Embodiment 1 only in the configuration and operation of the header multiplexing part 124, a description will be given in this respect alone.

Fig. 21 is a block diagram illustrating the configuration of the header multiplexing part of the VOP encoder according to Embodiment 7; the parts identical with those in Embodiment 1 of Fig. 4 are marked with the same reference numerals as in the latter, and no description will be repeated.

The operation of this embodiment will be described below. The bit stream with the VO header information multiplexed thereon in the VO header multiplexing part 1 is input into the VOL header multiplexing part 2. The VOL header multiplexing part 2 multiplexes on the input bit stream the VOL header information and a time code 18 forming the basis of time management, and outputs the bit stream to the GOV header multiplexing selection part 3.

The GOV header multiplexing selection part 3 determines the destination of the input bit stream from the VOL header multiplexing part 2 on the basis of the GOV multiplexing information 6 indicating whether to perform the multiplexing of the GOV header. When the GOV multiplexing information 6 indicates that the GOV header is not multiplexed, the bit stream is output to the VOP header multiplexing part 5. When the GOV multiplexing information 6 indicates that the multiplexing of the GOV header is performed, the bit stream is output to the GOV header multiplexing part 4.

In this instance, the GOV header multiplexing part 4 multiplexes the GOV header information on the bit stream fed from the GOV header multiplexing selection part 3, and outputs the bit stream to the VOP header multiplexing part 5.

The VOP header multiplexing part 5 multiplexes the VOP start code, the time management information header and the video information header onto the input bit stream, and outputs it to the video signal multiplexing part 126 (see Fig. 3).

Incidentally, the operations of the video signal multiplexing part 126 and the parts following it are the same as described above.

According to Embodiment 7, since the time code is multiplexed onto the VOL header which is always encoded in MPEG-4 as described above, it is possible to provide a bit stream which permits the creation of a pictorial image composed of a plurality of objects on the basis of the time code. Moreover, in the case of performing edits while decoding the encoded bit stream according to Embodiment 7 by a commercial object-by-object video editor, a VOP at an arbitrary time of objects can freely be accessed randomly at all times. These effects provide increased flexibility in image synthesis. Incidentally, while the encoder of this embodiment has been described to add the time code for each VOL, the encoder may also be configured to add the time code information for each VOP. This could be implemented by such a configuration as shown in Fig. 22 in which the time code 18 defining the absolute display time of each VOP is input into and multiplexed by a VOP header multiplexing part 301.

Furthermore, Embodiment 7 has been described to involve the encoding of the VOP rate information, but it is a matter of course that the multiplexing of the time is independent of the VOP rate information, and even when the VOP rate information is not encoded, the same effects as mentioned above are obtainable.

As described above, the image encoding device of Embodiment 7 which encodes images on the object-by-object basis is provided with absolute time multiplexing means by which information representing the absolute time of each object is multiplexed onto an encoded image signal.

EMBODIMENT 8

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A VOP decoder according to an eighth embodiment (Embodiment 8) of the present invention decodes the time code from the VOL header contained in the encoded bit stream. The VOP decoder is applicable to a system which synthesizes a plurality of decoded objects into an image by using a plurality of such VOP decoders.

A description will be given first of the configuration and operation of the VOP decoder in Embodiment 8. The internal configuration of the VOP decoder of this embodiment is depicted in Fig. 23. Since this decoder differs from the VOP decoder of Embodiment 2 only in the configuration and operation of a header analysis part 302, a description will be given below in this respect alone. The header analysis part 302 has a function of decoding and outputting the time code in the VOL header.

Fig. 24 illustrates the internal configuration of the header analysis part 302. Reference numeral 303 denotes a VOL header analysis part. The start code analysis part 51 analyzes the start code contained in the input encoded VOP bit stream 150. The start code analysis part outputs the bit stream to the VO header analysis part 52 when the analyzed start code indicates VO, to the VOL header analysis part 303 when the start code indicates VOL, to the GOV header analysis part 54 when the start code indicates GOV, and to the VOP header analysis part 55 when the start code indicates VOP. Incidentally, upon completion of the analysis in the VOP header analysis part 55, the bit stream is fed therefrom to the video signal analysis part 153. The VO header analysis part 52 analyzes the Vo header contained in the input bit stream, and outputs the analyzed bit stream to the start code analysis part 51. The VOL header analysis part 303 analyzes the VOL header information in the input bit stream, and outputs the analyzed bit stream to the start code analysis part 51. In this case, the time code 64 contained in the VOL header information is decoded and output. The GOV header analysis part 54 analyzes the GOV header information in the input bit stream, and outputs the analyzed bit stream to the start code analysis part 51. The VOP header analysis part 55 analyzes the VOP header information in the input bit stream, and outputs the analyzed bit stream via the start code analysis 51 to the video signal analysis part 153.

With the VOP decoder of the above configuration and operation, it is possible to output, for each VOL, the absolute display time of each VOP contained therein. In Fig. 25 there is depicted a system which uses this

information to synthesize a plurality of objects.

In Fig. 25, reference numeral 400 denotes an encoded VOP bit stream a, 401 an encoded VOP bit stream b, 402 an encoded bit stream c, 403 a VOP decoder for decoding the encoded VOP bit stream a400, 403b a VOP decoder for decoding the encoded VOP bit stream b401, 403c a VOP decoder for decoding the encoded VOP bit stream c402, 404 a decoded object image c, 405 a decoded object image b, 406 a decoded object image a, 407 a time code a, 408 a time code b, 409 a time code c, 410 a composition part, and 411 a decoded image. What is intended to mean by the decoded object image is an image obtained by combining the decoded shape data 156 and the corresponding decoded texture data 162 for each of VOPs and then integrating such combined pieces of data for each group of VOPs (for example, GOV or VOL).

The encoded VOP bit stream a400 to the encoded VOP bit stream c402 are decoded by the VOP decoder parts 403a to 403c corresponding thereto, respectively, by which the decoded VOP images a404 to c406 are generated. At this time, the VOP decoders decode the corresponding time codes a407 to c409, and output them to the composition part 210. Based on the time codes a407 to c409, the composition part 210 determines the time of the frame of the decoded image 411 where to synthesize the decoded VOP of each decoded object image, and maps them into the frame corresponding to the determined time. For example, assume the following situations.

* The composition part has a time code generation capability, and determines the absolute display time of each image frame to synthesize.

* Assume that 01:00:00 is decoded as the time code of the first VOP of the decoded object image a404, where 01:00:00 represents (hour):(minute):(second).

* Assume that 01:00:10 is decoded as the time code of the first VOP of the decoded object image b405.

* Assume that 01:01:00 is decoded as the time code of the first VOP of the decoded object image c406.

Assuming that the time code of the first image frame of the decoded image 411 defined in the composition part 410 is 01:00:00, the decoded object image a404 is mapped into the first frame of the decoded image 411, the decoded object image b405 is mapped 10 seconds after the first frame of the decoded image 411, and the decoded object image c406 is mapped one

minute after the first frame of the decoded image 411; thus, the decoded objects can be displayed in the respective frames. By this, it is possible to display a pictorial image with a plurality of video objects synthesized in the image frames in correspondence to the reference absolute times.

By using a plurality of such VOP decoders as described above, a simple-structured system can be implemented which synthesizes a plurality of object into a reconstructed image.

The time code may also be encoded for each VOL at the image encoding device side as depicted in Fig. 26. In this case, it is possible, at the image decoding device side, to decode the time code encoded for each VOL and synthesize a plurality of objects for each VOL as described above.

Fig. 27 shows a modification of the VOP decoder of the type that it is supplied with an encoded bit stream with the VOP rate multiplexed onto the VOL header together with the time code. With this configuration, it is possible to determine the absolute display time of the first VOP of the VOL by the time code and then detect the absolute display time of each VOP from the VOP rate information--this allows more ease in implementing a system that synthesizes a plurality of objects.

While in the above a plurality of VOP decoders of this embodiment employs have been described as being applied to the system for synthesizing a plurality of objects, only one such VOP decoder may be used in a system that decodes only one object to reconstruct an image.

As described above, according to Embodiment 8, the image decoding device which decodes the bit stream encoded from an image on an object-by-object basis is provided with: absolute time analysis means for analyzing, for each object, information indicating the absolute time therefor; and means for reconstructing the image processed on the object-by-object basis through utilization of the information indicating the absolute time analyzed by the absolute time analysis means.

EMBODIMENT 9

A ninth embodiment (Embodiment 9) of the present invention is directed to a VOP encoder that implements an improved scheme for encoding the modulo time base (corresponding to first time information) which is used in combination with the VOP time increment (corresponding to second time

information) in MPEG-4.

A description will be given first of how the modulo time base is indicated in MPEG-4.

As described previously in Embodiment 1, the value of the modulo time base is information that indicates what number of seconds will pass until the VOP concerned is displayed after a certain reference time as shown in Fig. 5, and the information expresses the number of seconds in terms of the number of bits of the value "1." The end of the data is clearly indicated by the value "0" added thereto. That is, when the display is provided after 5 seconds, the information becomes "111110." With this method, when the reference time does not change at all, the amount of information of the modulo time base increases infinitely. At present, MPEG-4 defines the reference time by the time code that is multiplexed onto the GOV header, but since the GOV header is an option, the GOV header need not always be encoded under MPEG-4 prescriptions. For this reason, there is a fear that the value of the modulo time base becomes longer limitlessly unless the GOV header appears. The encoder of this embodiment obviates such a problem in encoding the data of the modulo time base.

Since the encoder of this embodiment can be implemented by changing the configuration and operation of only the header multiplexing part 124 in the VOP encoders described above, a description will be given in this respect alone.

Fig. 28 illustrates the internal configuration of the header multiplexing part 124 in Embodiment 9. Reference numeral 500 denotes a VOP header multiplexing part, 19 a bit length calculating part, 20 a modulo time base, 21 a shifted modulo time base, 22 an information bit indicating a repeat count, and 501 a modulo time base multiplexing part.

Next, the operation of this embodiment will be described. The bit stream with the VO header information multiplexed thereon in the VO header multiplexing part 1 is input into the VOL header multiplexing part 2. The VOL header multiplexing part 2 multiplexes the VOL header information onto the input bit stream, and outputs the multiplexed bit stream to the GOV header multiplexing selection part 3.

The GOV header multiplexing selection part 3 determines the destination of the bit stream from the VOL header multiplexing part 2 according to the GOV multiplexing information 6 indicating whether to perform

multiplexing of the GOV header. When the GOV multiplexing information 6 indicates that the GOV header is not multiplexed, the bit stream is output to the VOP header multiplexing part 5. When the GOV multiplexing information 6 indicates that the GOV header is multiplexed, the bit stream is output to the GOV multiplexing part 4. In this case, the GOV header multiplexing part 4 multiplexes the GOV header information onto the bit stream from the GOV header multiplexing selection part 3, and outputs the multiplexed bit stream to the VOP header multiplexing part 5.

The VOP start code multiplexing part 8 in the VOP header multiplexing part 500 multiplexes the VOP start code onto the input bit stream, and outputs the multiplexed bit stream to the modulo time base multiplexing part 501. The bit length calculating part 19 in the VOP header multiplexing part 500 compares the bit length of the modulo time base 20 and a preset positive threshold value. When the bit length of the modulo time base 20 is longer than the threshold value, the modulo time base 20 is left-shifted repeatedly by the length of the threshold value until the bit length of the modulo time base becomes shorter than the threshold value. The bit length calculating part 10 outputs the thus shifted modulo time base 21 in the form of a bit string and the information bit 22 which indicates the shift-repeat count. The information bit 22 indicating the shift-repeat count may be provided as a binary number that expresses the shift-repeat count by a predetermined number of bits, or as a variable bit length that expresses the shift-repeat count by a variable-length code.

The operation of the bit length calculation part 19 will be concretely described below. With the abovesaid threshold value set at 4, if the modulo time base 20 is "111111110," the shift-repeat count is two and the shifted modulo time base 21 is "10." If expressed by a fixed two-bit length, the information bit 22 indicating the shift-repeat count is "10."

The modulo time base multiplexing part 501 in the VOP header multiplexing part 500 multiplexes onto the bit stream from the VOP start code multiplexing part 8 the shifted modulo time base 21 and the information bit 22 indicating the shift-repeat count, and outputs the multiplexed bit stream to the VOP time increment multiplexing part 10.

The VOP time increment multiplexing part 10 multiplexes the VOP time increment onto the bit stream from the modulo time base multiplexing part 10, and outputs the multiplexed bit stream to the video information header

multiplexing part 11. The video information header multiplexing part 11 multiplexes the video information header onto the bit stream from the VOP time increment multiplexing part 10, and outputs the multiplexed bit stream to the video signal multiplexing part 26.

As described above, according to Embodiment 9, the modulo time base is expressed by two kinds of information bits (the shifted modulo time base and the information bit indicating the shift-repeat count). And these two kinds of information bits are multiplexed instead of multiplexing the modulo time base expressed as prescribed in MPEG-4 at present. Hence, the VOP encoder of this embodiment permits suppression of the amount of information generated suppressed as compared with that in the case of using the method according to MPEG-4.

As described above, the image encoding device of Embodiment 9 which encodes images on the object-by-object basis is provided with time information encoding means which encodes, as information defining the display time of an image at each time on the object-by-object basis, the first time information defining the time interval between the reference time and the display time, the second information defining the display time with a higher accuracy than that of the time defined by the first time information and the image corresponding to each time. The time information encoding means expresses the first time information by conversion into a bit length. When the bit length of the first time information is longer than a predetermined set value, a bit shift corresponding to the set value is repeated until the bit length becomes shorter than the set value, and at the same time, the number of bit shifts is counted. Then the shift-repeat count and the bit string obtained by the repetitions of the bit shift are encoded.

EMBODIMENT 10

A tenth embodiment (Embodiment 10) of the present invention is directed to a VOP decoder which decodes the modulo time base information multiplexed onto the encoded bit stream in the modulo time base multiplexing part described above in Embodiment 9 and uses the decoded information and the VOP time increment to define the display time of each VOP.

Since the VOP decoder of this embodiment differs from the VOP decoders

described so far only in the configuration and operation of the header analysis part 151, a description will be given in this respect alone.

Fig. 29 illustrates the internal configuration of the header analysis part 151 of the VOP decoder according to Embodiment 10. Reference numeral 502 denotes a VOP header analysis part, 65 a modulo time base analysis part, 66 a VOP time increment analysis part, 67 a modulo time base calculation part, 69 a shifted modulo time base, and 70 an information bit indicating a shift-repeat count.

Next, the operation of this embodiment will be described. The start code analysis part 51 analyzes the start code contained in an encoded bit stream having multiplexed thereon the input shifted modulo time base 69 and the information bit 70 indicating the shift-repeat count. The start code analysis part 51 outputs the bit stream 152 to the VO header analysis part 52 when the analyzed start code is contained in the VO header, to the VOL header analysis part 53 when the start code is contained in the VOL header, to the GOV header analysis part 54 when the start code is contained in the GOV header, to the VOP header analysis part 55 when the start code is contained in the VOP header, and to the video signal analysis part 153 (see Fig. 11) when the start code is contained in the VOP data information. The operations of the video signal analysis part and the parts following it are the same as described so far.

The modulo time base analysis part 65 in the VOP header analysis part 502 analyzes the shifted modulo time base 69 and the information bit 70 indicating the shift-repeat count contained in the bit stream fed from the start code analysis part 51, and outputs the shifted modulo time base 69 and the information bit 70 indicating the shift-repeat count to the modulo time base calculation part 67 and the bit stream to the VOP time increment analysis part 66.

The modulo time base calculation part 67 calculates the modulo time base from the shifted modulo time base 69 and the information bit 70 indicating the shift-repeat count, and outputs it to the composition part 210. More specifically, the value of the modulo time base is restored by reversing the procedure described previously with reference to Embodiment 9. In the case where a preset positive threshold value (The decoder side also required to set exactly the same value as the threshold value described in respect of the encoder of Embodiment 9.) and the shifted modulo time base 69 is "10"

and the information bit 70 indicating the shift-repeat count is "10," "1111111110" with "11111111" added to the high-order bit of "10" is the restored value of the modulo time base. The thus obtained restored value of the modulo time base is used to define the display time of the VOP concerned, together with the VOP time increment information.

The VOP time increment analysis part 66 analyzes the VOP time increment contained in the bit stream fed from the modulo time base analysis part 65, and outputs the analyzed bit stream to the video information header analysis part 57. The video information header analysis part 57 analyzes the video information header contained in the bit stream fed from the VOP time increment analysis part 66, and outputs the analyzed bit stream to the video signal analysis part 153.

As described above, the decoder of Embodiment 10 is configured to calculate the modulo time base from the two kinds of information bits (the shifted modulo time base and the information indicating the shift-repeat count); hence it is possible to analyze the bit stream described later in Embodiment 12 which has a smaller amount of information generated than that by the encoded representation prescribed in MPEG-4.

As described above, the image decoding device of Embodiment 10 which decodes a bit stream with images encoded on the object-by-object basis is provided with: time information decoding means which decodes, as information defining the display time of an image at each time on the object-by-object basis, the first time information defining the time interval between the reference time and the display time, the second information defining the display time with a higher accuracy than that of the time defined by the first time information, and the image corresponding to each time; and decoding and synthesizing means which decodes the input encoded image signal on the object-by-object basis and synthesizes these decoded image signals. The time information decoding means decodes the bit-shift repeat count and the shifted bit string and decodes the first time information by adding the bit string with a code of the length of the predetermined set value by the number of bit-shift repetitions, and the decoding and synthesizing means synthesizes the decoded image signal on the basis of the first and second time information decoded by the time information decoding means.

EMBODIMENT 11

An eleventh embodiment (Embodiment 11) of the present invention is a VOP encoder that implements another improved scheme for encoding the modulo time base which is used in combination with the VOP time increment in MPEG-4.

Since the VOP encoder of this embodiment differs from the VOP encoders described so far only in the configuration and operation of the header multiplexing part 124, a description will be given in this respect alone.

Fig. 30 illustrates the internal configuration of the header multiplexing part 124 of the VOP encoder according to Embodiment 11. Reference numeral 503 denotes a VOP header multiplexing part, 23 a modulo time base holding part, 24 a difference modulo time base generating part, 25 a difference modulo time base multiplexing part, and 26 a difference modulo time base.

The VOP start code multiplexing part 8 in the VOP header multiplexing part 503 multiplexes the VOP start code onto the input bit stream, and outputs the multiplexed bit stream to the difference modulo time base multiplexing part 25.

The modulo time base holding means 23 in the VOP header multiplexing part 503 holds the value of the modulo time base of the immediately previously encoded VOP, and after modulo time base of the immediately preceding encoded VOP is output therefrom, the modulo time base of the VOP to be encoded is written in the modulo time base holding part.

The difference modulo time base generating part 24 in the VOP header multiplexing part 503 calculates a bit string of the difference between the modulo time base of the immediately preceding encoded VOP input thereinto from the modulo time base holding part 23 and the modulo time base of the VOP to be decoded. Then the difference modulo time base generating part 24 calculates the difference modulo time base 26 based on the number of bits "1" contained in the calculated difference bit string, and outputs it to the difference modulo time base multiplexing part 25.

Now, a concrete example of the generation of the difference modulo time base will be described.

In the case where the modulo time base of the immediately previously encoded VOP is "11110" (decimal numeral: 30) and the modulo time base of the VOP to be encoded is "111110" (decimal numeral: 62), the difference bit

string becomes "100000" (decimal numeral: 32). Then, the number of bits "1" contained in the thus calculated difference bit string "100000" is one. In the case of calculating the difference modulo time base by such a conversion table as Table 2, the difference modulo time base corresponding to one bit "1" is "10," and consequently, "10" is output as the difference modulo time base. Table 2 is an example of the conversion table, and other conversion tables may also be defined.

Also it is possible to obtain the difference modulo time base simply by making a comparison of bit lengths alone. For example, in the above example the bit length of the modulo time base of the immediately previously encoded VOP is 5 and the bit length of the modulo time base of the VOP to be encoded is 6; therefore, a value of 1 is obtained as the difference. By using this value as a substitute for the "number of bits "1" contained in the difference bit string" in Table 2, the difference modulo time base can be expressed.

The difference modulo time base multiplexing part 25 in the VOP header multiplexing part 503 multiplexes the difference modulo time base 26 onto the input bit stream, and outputs the multiplexed bit stream to the VOP time increment multiplexing part 10.

The VOP time increment multiplexing part 10 in the VOP header multiplexing part 503 multiplexes the VOP time increment onto the bit stream fed from the difference modulo time base multiplexing part 25, and outputs the multiplexed bit stream to the video information header multiplexing part 11.

As described above, the encoder according to Embodiment 11 is adapted to express the modulo time base as the difference modulo time base and multiplex the difference modulo time base instead of encoding the modulo time base in the form presently prescribed in MPEG-4; hence, the amount of information generated can be made smaller than in the case of using the method prescribed in MPEG-4.

As described above, the image encoding device of Embodiment 11 which encodes images on the object-by-object basis is provided with time information encoding means which encodes, as information defining the display time of an image at each time on the object-by-object basis, the first time information defining the time interval between the reference time and the display time, the second information defining the display time with a

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EMBODIMENT 12

A twelfth embodiment (Embodiment 12) of the present invention is directed to a VOP decoder which restores the value of the modulo time base of the VOP concerned from information about the difference modulo time base multiplexed onto the encoded bit stream in the difference modulo time base multiplexing part 25 described above in Embodiment 11 and uses the restored modulo time base value to define the display time of each VOP.

Since the VOP decoder of this embodiment differs from the VOPs described so far only in the configuration and operation of the header analysis part 151, a description will be given in this respect alone.

Fig. 31 illustrates the internal configuration of the header analysis part 151 of the VOP decoder according to Embodiment 12. Reference numeral 504 denotes a VOP header analysis part, 71 a difference modulo time base analysis part, 72 a modulo time base generating part, 73 a VOP time increment analysis part, 74 a modulo time base holding part, and 75 a difference modulo time base.

The difference modulo time base analysis part 71 in the VOP header analysis part 504 analyzes the difference modulo time base 75 contained in a bit stream fed from the start code analysis part 51, and outputs the analyzed difference modulo time base 75 to the modulo time base generating part 72 and the analyzed bit stream to the VOP time increment analysis part 73.

The modulo time base generating part 72 in the VOP header analysis part 504 calculates the number of bits "1" contained in the bit string of the difference between the modulo time base of the immediately previously

504 analyzed the VOP time increment contained in the bit stream fed from the difference modulo time base analysis part 71, and outputs the analyzed bit stream to the video information header analysis part 57.

As described above, the decoder of Embodiment 12 is adapted to calculate the modulo time base from the difference time modulo base with a small amount of information; hence it is possible to analyze the bit stream described previously in Embodiment 8 which has a smaller amount of information generated than that by the encoded representation prescribed in MPEG-4.

As described above, the image decoding device of Embodiment 12 which decodes a bit stream with images encoded on the object-by-object basis is provided with: time information decoding means which decodes, as information defining the display time of an image at each time in an image series, the first time information defining the time interval between the reference time and the display time and second information defining the display time with a higher accuracy than that of the time defined by the first time information and the image corresponding to each time; and decoding and synthesizing means for decoding the input encoded image signal on the object-by-object basis and synthesizing these decoded image signals. The time information decoding means holds the first time information of the immediately previously decoded image, then adds the first time information of the immediately previously decoded image available from the first time information holding means to a bit string decoded as the first time information of the image to be decoded, thereby decoding the first time information of the image to be decoded. The decoding and synthesizing means synthesizes the decoded image signal on the basis of the first and second time information decoded by the time information decoding means.

EMBODIMENT 13

While in the above there have been described the image encoding device of the type that multiplexes the display speed information onto the encoded image signal and the image encoding device of the type that multiplexes the absolute time information onto the encoded image signal, it is also possible to implement an image encoding device which multiplexes both the display

speed information and the absolute time information onto the encoded image signal.

This can be done by a parallel or series arrangement of display speed information multiplexing means and absolute time information multiplexing means in the respective image encoding device described above.

The same goes for the image decoding device side. To put it simply, there have been described above the image decoding device of the type that decodes the display speed information and uses this decoded display speed information to reconstruct images processed on the object-by-object basis and the image decoding device of the type that decodes the absolute time information and uses the decoded absolute time information to reconstruct images processed on the object-by-object basis. It is also possible, however, to implement an image decoding device which reconstructs the images processed for each object on the basis of the display speed information and the absolute time information.

This can be done by a parallel or series arrangement of the display speed information decoding part and the absolute time information decoding part in the respective decoding device described above so that images processed for each object are reconstructed based on the information decoded in each decoding part.

With the above configuration, the image restoration and synthesis can be performed more smoothly and more accurately.

EMBODIMENT 14

While in the there have been described the image encoding device of the type that multiplexes the display speed information on the encoded image signal and the image encoding device of the type that encodes and multiplexes the first time information, the second time information and the image, it is also possible to implement an image encoding device which encodes and multiplexes the display speed information, the first time information, the second time information and the image.

This can be done by a parallel or series arrangement of display speed information multiplexing means and first and second time information and image multiplexing means in the image encoding device described so far.

The same goes for the image decoding device side. To put it briefly, there have been described above the image decoding device of the type that decodes the display speed information and, based on the decoded display speed information, reconstructs images processed for each object and the image decoding device of the type that decodes the first time information, the second time information and the image and, based on the decoded first time information, second time information and image, reconstructs the image. It is also possible, however, to implement an image decoding device which reconstructs images on the basis of the display speed information, the decoded first and second time information.

This can be done by a parallel or series arrangement of the display speed information decoding part and the time information decoding part in the respective image decoding device described above so that images processed for each object are reconstructed based on the information decoded in each decoding part (means).

With the above configuration, the image restoration can be performed more smoothly and more accurately with a small amount of coded information sent.

EMBODIMENT 15

While in the above there have been described the image encoding device of the type that multiplexes the absolute time information and encoded image signal and the image encoding device of the type that encodes and multiplexes the first time information, the second time information and the image, it is also possible to implement an image encoding device which encodes and multiplexes the absolute time information, the first and second time information and the image.

This can be done by a parallel or series arrangement of absolute time multiplexing means and first and second time information and image encoding and multiplexing means in the respective image encoding device described so far.

The same goes for the image decoding device side. To put it simply, there have been described above the image decoding device of the type that decodes the absolute time information and, based on the decoded absolute time information, reconstruct images processed for each object and the

image decoding device of the type that decodes the first time information, the second time information and the image and reconstruct the image, based on the decoded first time information, second time information and image. It is also possible, however, to implement an image decoding device which reconstructs images on the basis of the absolute time information and the decoded first and second time information.

This can be done by a parallel or series arrangement of the absolute time information decoding part and the time information decoding part in the respective image decoding device described above so that images processed for each object are reconstructed based on the information decoded in each decoding part (means).

With the above configuration, the image restoration can be achieved more smoothly and more accurately with a small amount of coded information to send.

INDUSTRIAL APPLICABILITY

As described above, according to the present invention, the image decoding device analyzes the display speed information multiplexed in the image encoding device and performs decoding based on the analyzed display speed information, thereby permitting smooth image reconstruction with a simple structure. Furthermore, the image decoding device decodes the absolute time information multiplexed in the image encoding device and performs decoding based on the analyzed absolute time information, thereby permitting the image reconstruction with ease and with high accuracy. Moreover, the image decoding device decodes the first and second time information encoded in the image encoding device and decodes the input image signal based on the decoded first and second time information, thereby permitting the reception of the image signal sent with a small amount of information.

TABLE.1

VOP RATE	VOP RATE INFORMATION
30/SEC	01
15/SEC	10
STILL PICTURE	00
VARIABLE	11

TABLE.2

NUMBER OF BITS "1" CONTAINED IN DIFFERENCE BIT STRING	DIFFERENCE MODULO TIME BASE
0	0
1	10
2	110
⋮	⋮
n	11.....10

"1" CONTINUES FOR n BITS

TABLE.3

VOP RATE	VOP RATE INFORMATION
2/SEC	000
5/SEC	001
25/SEC	010
30/SEC	011
FIXED VOP RATES OTHER THAN THE ABOVE (2/SEC, 5/SEC, 25/SEC, 30/SEC)	100

TABLE.4

VOP RATE	VOP RATE INFORMATION
2/SEC	000
5/SEC	001
25/SEC	010
30/SEC	011
FIXED VOP RATES OTHER THAN THE ABOVE (2/SEC, 5/SEC, 25/SEC, 30/SEC)	100
STILL PICTURE	101

CLAIM

1. An image encoding device which encodes images for each object, comprising:

encoding means for encoding said images on the basis of predetermined object display speed information to obtain encoded image signal; and multiplexing means for multiplexing said predetermined object display speed information onto said encoded image signal.

2. The image encoding device of claim 1, wherein said multiplexing means multiplexes said object display speed information onto said encoded image signal for each object.

3. The image encoding device of claim 1, wherein said multiplexing means multiplexes onto said encoded image signal, as said object display speed information, information indicating whether the object display speed is fixed or variable.

4. The image encoding device of claim 1, wherein said multiplexing means multiplexes onto said encoded image signal, as said object display speed information, a value indicating the object display speed.

5. The image encoding device of claim 4, wherein said object display speed information includes a value indicating an inherent fixed object display speed and a value indicating an arbitrary fixed object display speed.

6. An image decoding device which decodes an encoded bit stream formed by encoding images for each object, comprising: display speed information decoding means for decoding object display speed information from said encoded bit stream; and control means for controlling the reconstruction of said encoded images encoded for each object, based on said decoded object display speed information.

7. The image decoding device of claim 6, wherein said display speed information decoding means decodes said object display speed information

for each object.

8. The image decoding device of claim 6, wherein: said display speed information decoding means decodes said object display speed information from said encoded bit stream for each object; and said control means controls said image reconstruction, depending upon whether the object display speed indicated by said decoded object display speed information is a fixed or variable speed.

9. The image decoding device of claim 8, wherein: said display speed information decoding means decodes said object display speed information from said encoded bit stream for each object; and said control means controls said image reconstruction by specifying the display time of each image on the basis of the value of said object display speed in said decoded object display speed information when said object display speed information decoded by said display speed information decoding means indicates a fixed speed, and on the basis of display time information multiplexed for each image when said decoded object display speed information indicates a variable speed.

10. The image decoding device of claim 6, wherein: said display speed information decoding means decodes said object display speed information from said encoded bit stream for each object; and said control means controls said image reconstruction by specifying the display time of each object on the basis of said object display speed information when said object display speed information decoded by said display speed information decoding means indicates a fixed speed and said fixed speed is a value indicated by said object display speed information, on the basis of display time information multiplexed for each image when said decoded object display speed information indicates a fixed speed and said fixed speed is not indicated as a specific value, and on the basis of said display time information multiplexed for each image when said decoded object display speed information indicates a variable speed.

11. The image decoding device of claim 6, wherein said control means is provided with: decoding time specifying means for specifying object

decoding time on the basis of the decoded object display speed information decoded by the display speed information decoding means and object display speed information preset in the decoding device; and decoding means for performing object decoding on the basis of the object decoding time specified by said decoding time specifying means.

12. An image encoding device which encodes an image for each object, comprising:

absolute time multiplexing means for multiplexing, for each object, information indicating absolute time for said object onto said encoded image signal.

13. An image decoding device which decodes an encoded bit stream formed by encoding images for each object, comprising: absolute time analysis means for analyzing, for each object, information indicating absolute time of said each object; and

control means for controlling the reconstruction of said encoded images on the basis of said absolute time information analyzed by said absolute time analysis means.

14. An image encoding device which encodes an image for each object, comprising:

time information encoding means for encoding, as information defining the display time of an image at each time for each object, first time information defining the time interval between a reference time and said display time and second time information defining said display time with higher accuracy than by said first time information, and an image corresponding to each time;

wherein when the bit length of said first time information is longer than a predetermined set value, said time information encoding means repeats a bit shift of said set value until said bit length becomes shorter than said set value and keeps count of bit shifts and encodes said count and a bit string obtained by the repeated bit shifts.

15. An image encoding device which encodes images for each object, comprising:

time information encoding means for encoding, as information defining the display time of an image at each time for each object, first time information defining the time interval between a reference time and said display time and second time information defining said display time with higher accuracy than by said first time information, and an image corresponding to each time;

wherein said time information encoding means includes first time information holding means for holding said first time information of an image encoded at immediately preceding time and calculates a bit string of the difference between said first time information of an image to be encoded and said first time information of said image at said immediately preceding time available from said first time information holding means and then encodes said difference bit string as said first time information of said image to be encoded.

16. An image decoding device which decodes a bit stream formed by encoding images for each object, comprising:

time information decoding means for decoding, as information defining the display time of an image at each time for each object, first time information defining the time interval between a reference time and said display time and second time information defining said display time with higher accuracy than by said first time information and an image corresponding to each time; and

decoding and synthesizing means for decoding input encoded image signals for each object and for synthesizing said decoded image signals, wherein said time information decoding means decodes, as encoded data of said first time information, a count of bit shifts of said first time information and a bit string obtained by repeated bit shifts and decodes said first time information by adding a code of a length of a predetermined set value to said bit string by said count of bit shifts, and said decoding and synthesizing means synthesizes a decoded image signal on the basis of said first and second time information decoded by said time information decoding means.

17. An image decoding device which decodes a bit stream formed by encoding an image for each object, comprising:

time information decoding means for decoding, as information defining the

display time of an image at each time in an image sequence, first time information defining the time interval between a reference time and said display time and second time information defining said display time with higher accuracy than by said first time information and an image corresponding to each time; and

decoding and synthesizing means for decoding input encoded image signals for each object and for synthesizing said decoded image signals;

wherein said time information decoding means includes first time information holding means for holding said first time information of an image encoded at immediately preceding time and decodes said first time information of an image to be decoded after adding a bit string decoded as said first time information of said image to be decoded with said first time information of the immediately previously decoded image available from said first time information holding means, and said decoding and synthesizing means synthesizes said decoded image signals on the basis of said first and second time information decoded by said time information decoding means.

18. An image encoding method, comprising the steps of: generating an encoded image signal by encoding images for each object on the basis of predetermined object display speed information; and

outputting said encoded image signal and said object display speed information after multiplexing them.

19. An image decoding method, comprising the steps of: decoding an object display speed information from an encoded bit stream formed by encoding images for each object; and

decoding said encoded images by controlling the reconstruction thereof for each object on the basis of said decoded object display speed information.

ABSTRACT OF THE DISCLOSURE

Multiplexing means of an image encoding device multiplexes object display speed information or image display absolute time information, and an image decoding device performs image processing on the basis of the multiplexed object display speed information or image display absolute time information, whereby image decoding process can be performed smoothly and accurately.

09545172.040600

FIG.1

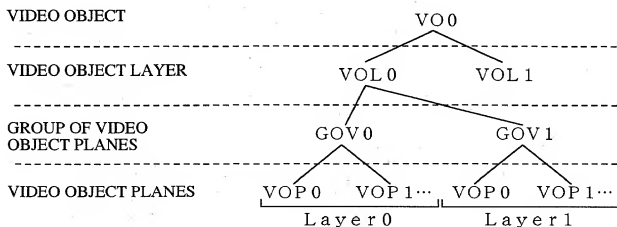


FIG.5

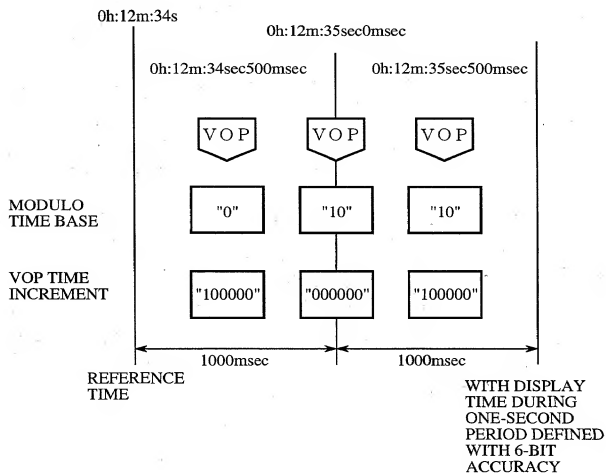


FIG. 2

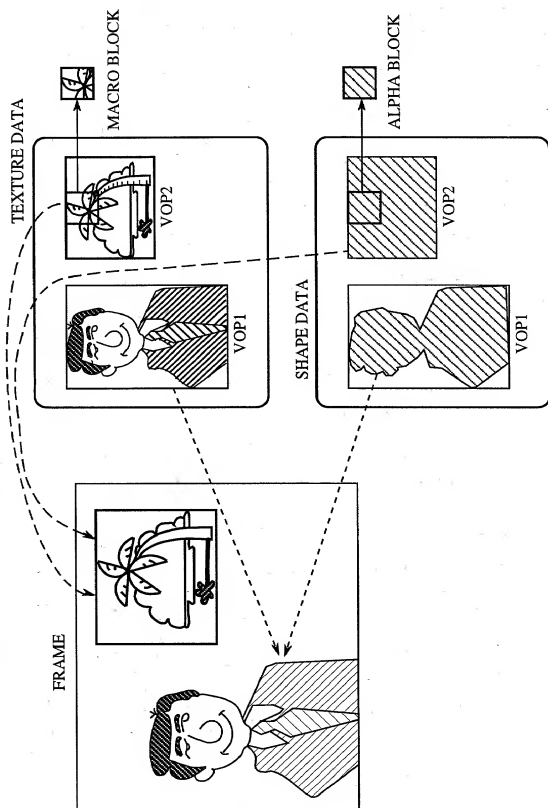
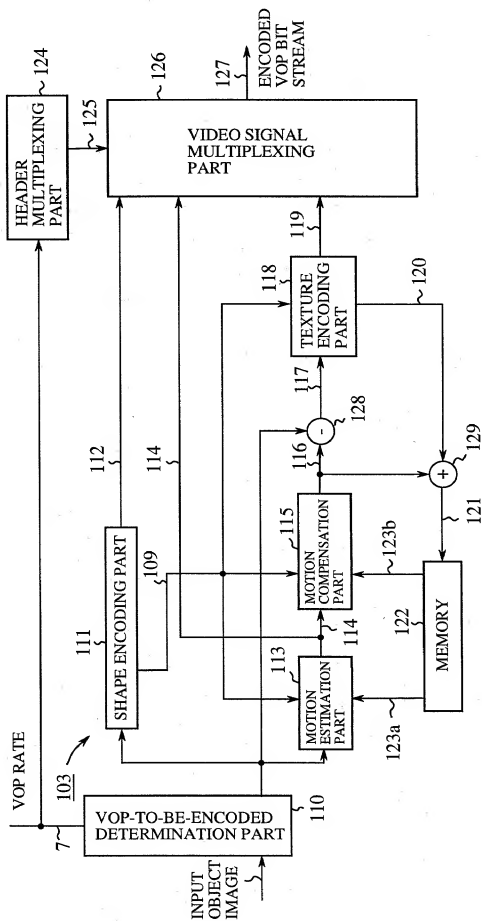
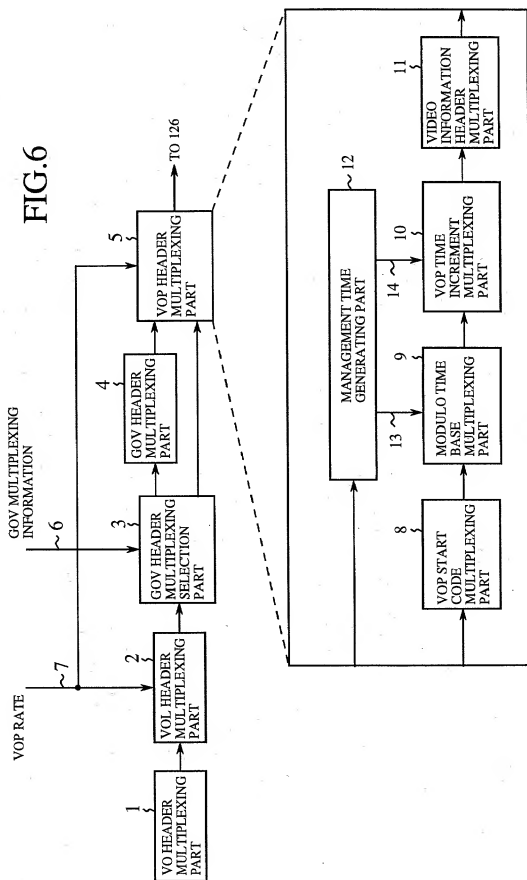


FIG.3



[illegible]

FIG. 6



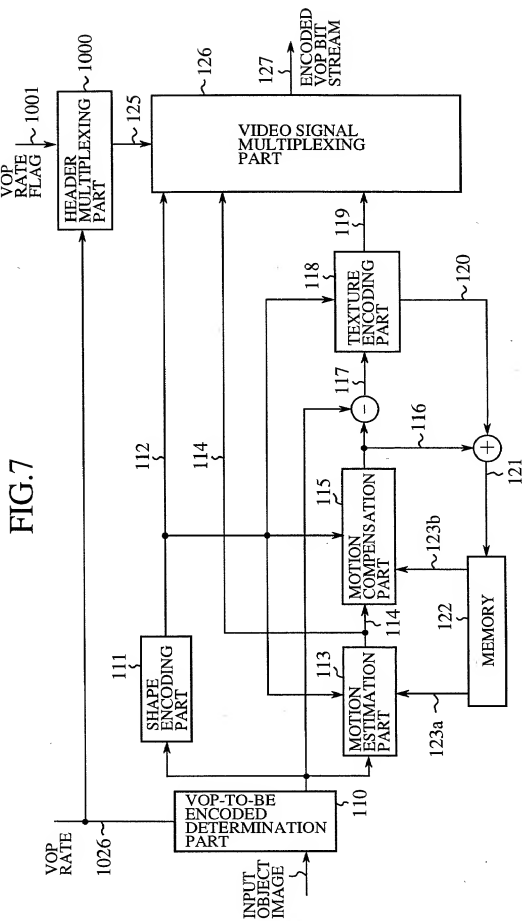


FIG.9

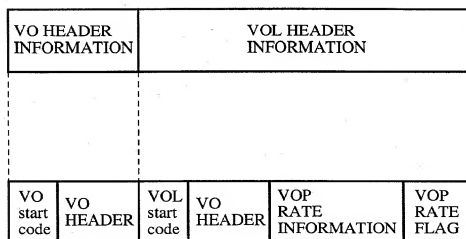


FIG.10

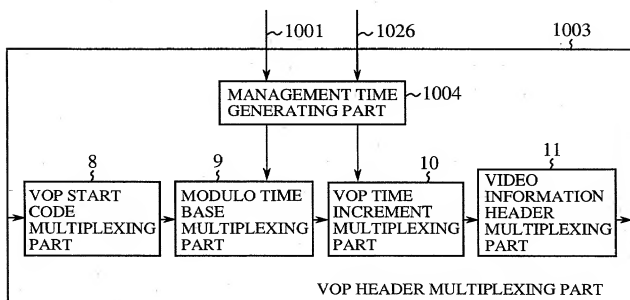


FIG.11

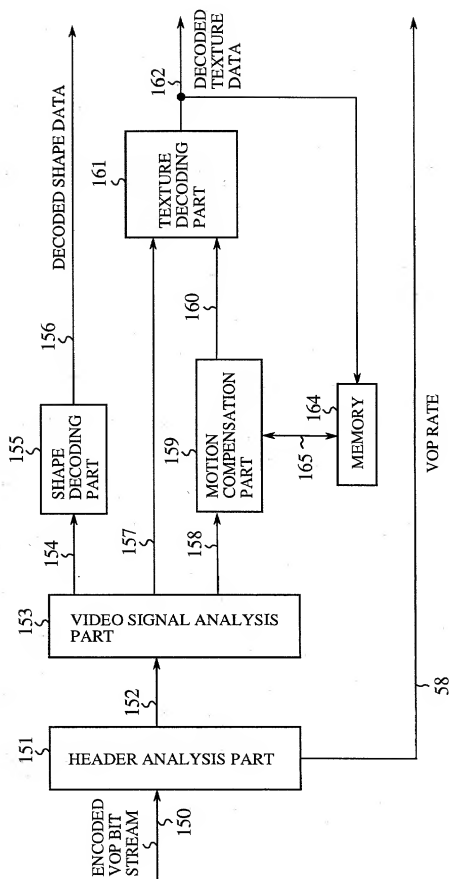


FIG.12

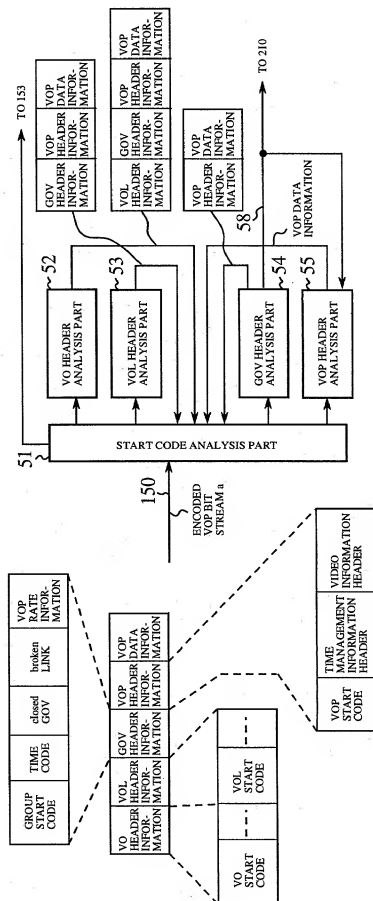
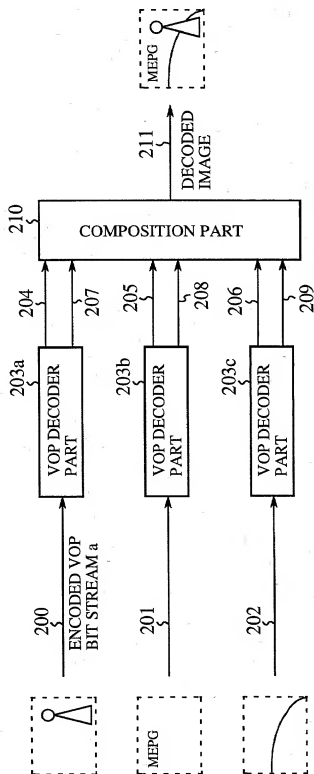


FIG.13



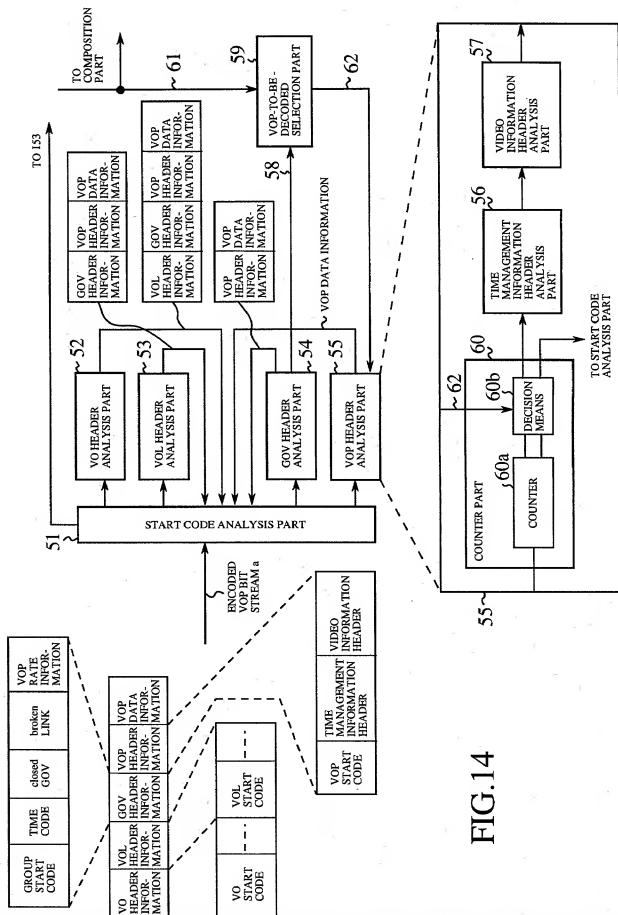


FIG.14

FIG.15

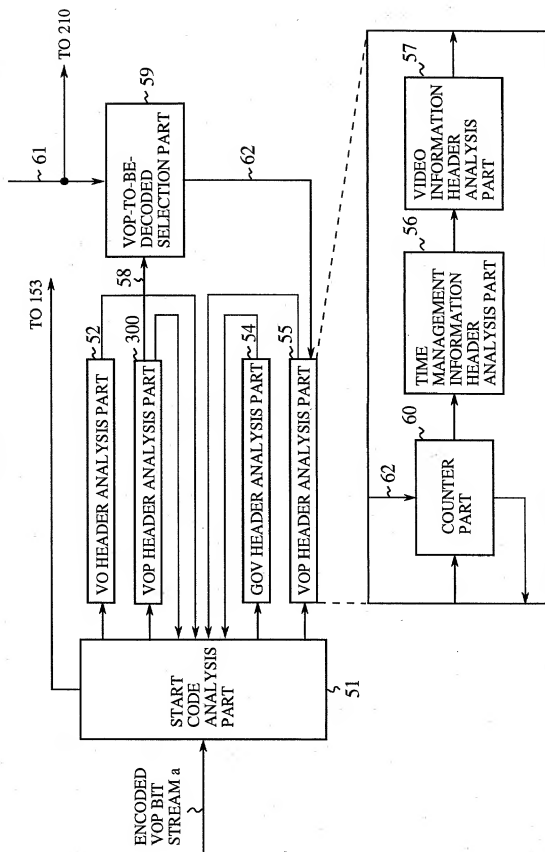


FIG.16

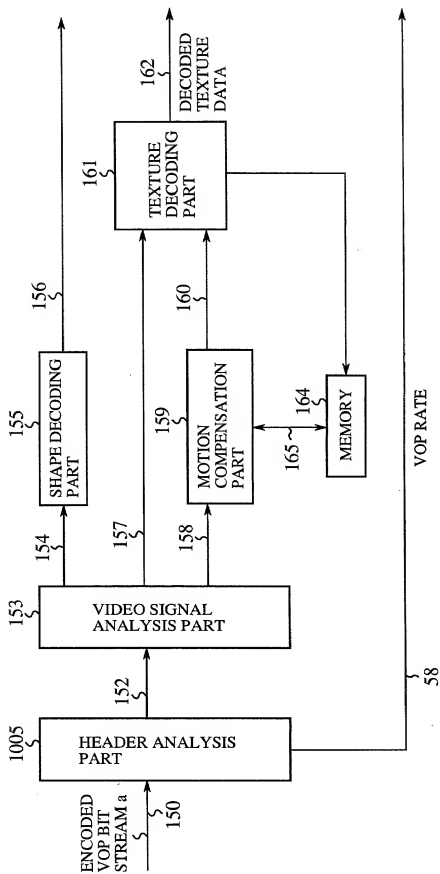


FIG.17

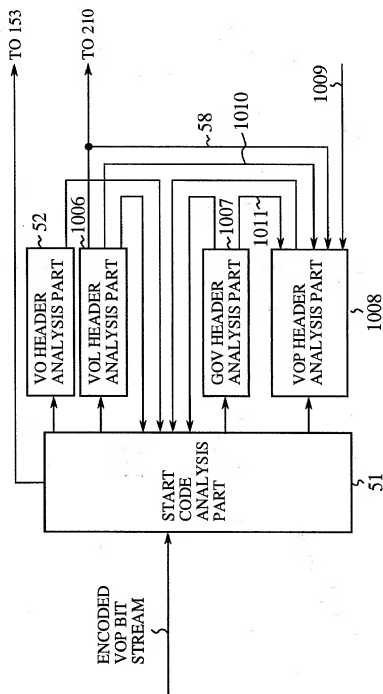


FIG.18

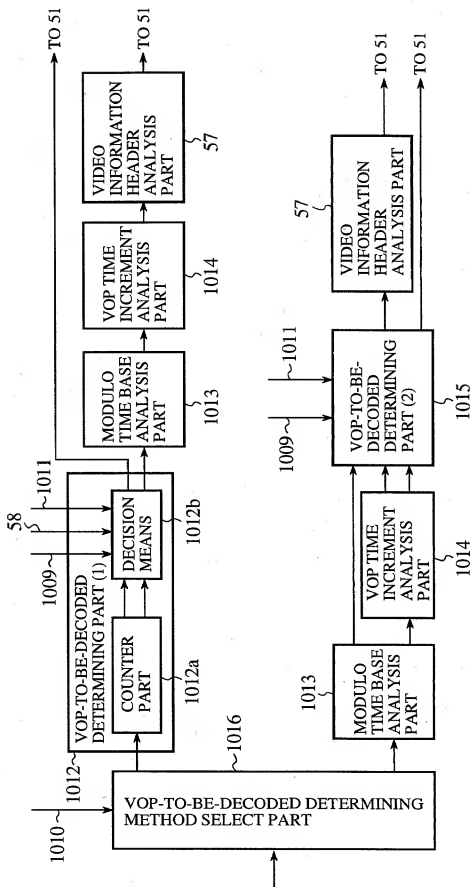


FIG.19

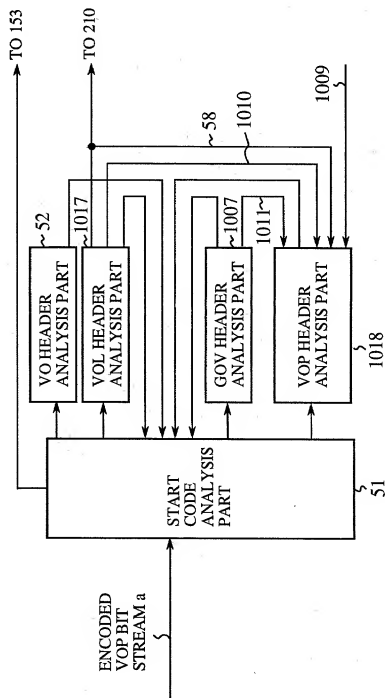


FIG.20

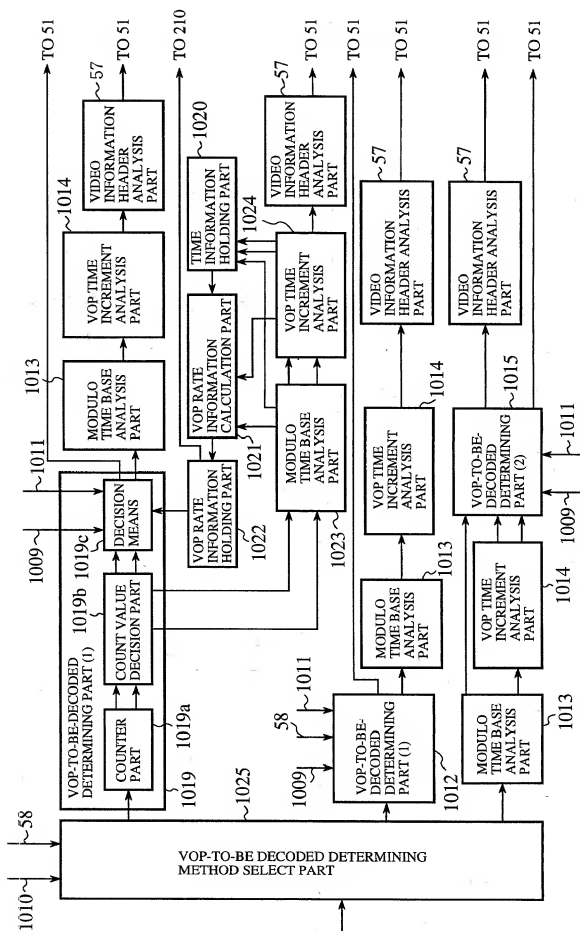


FIG.21

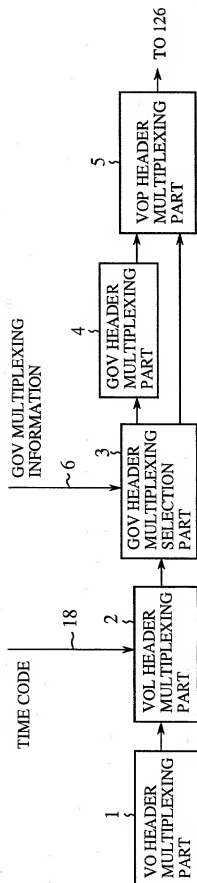


FIG.22

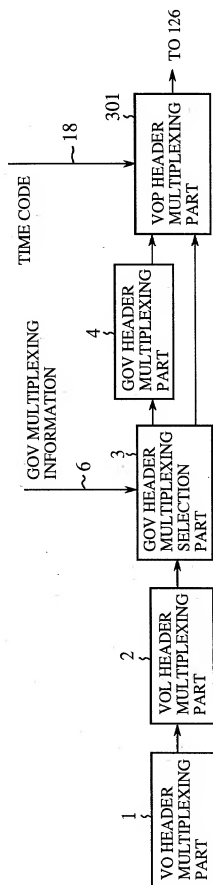


FIG.23

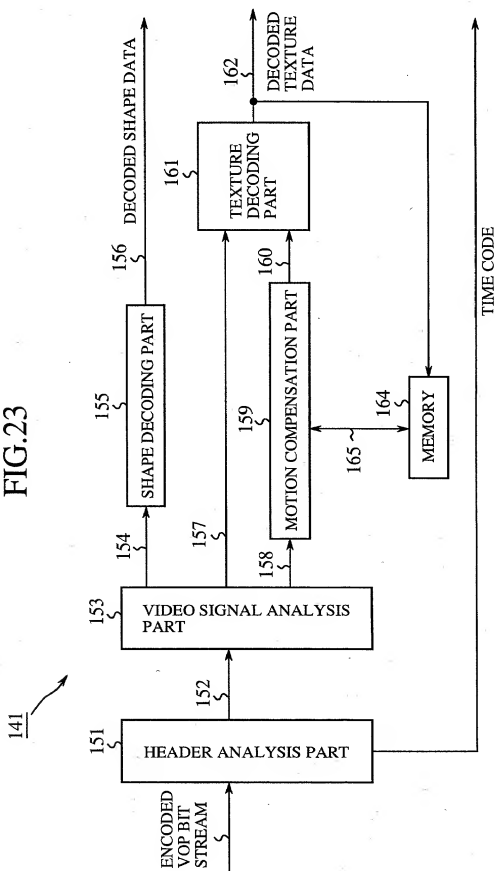


FIG.24

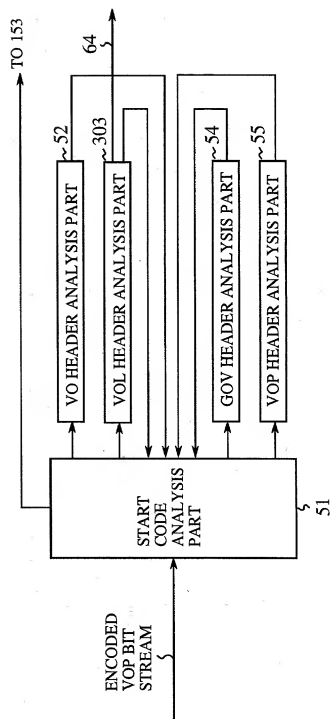


FIG.25

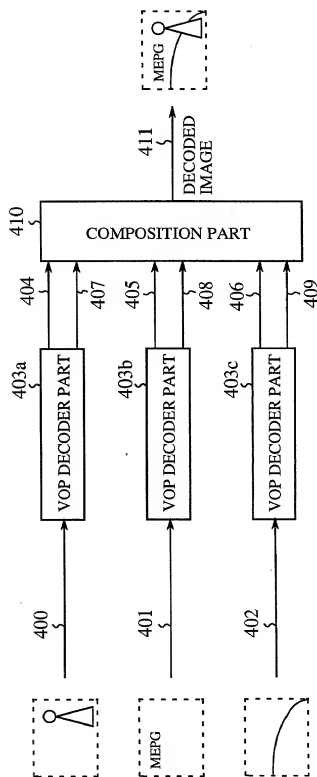


FIG. 26

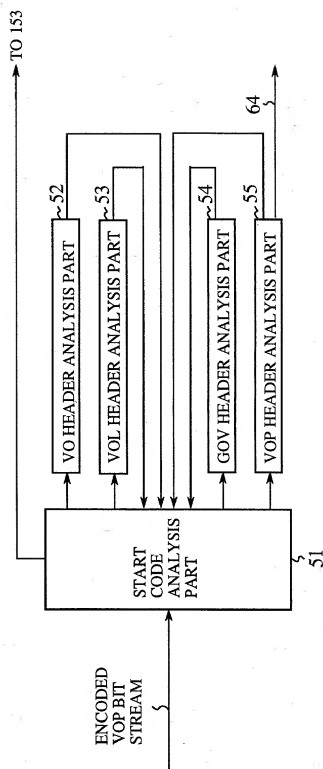


FIG. 27

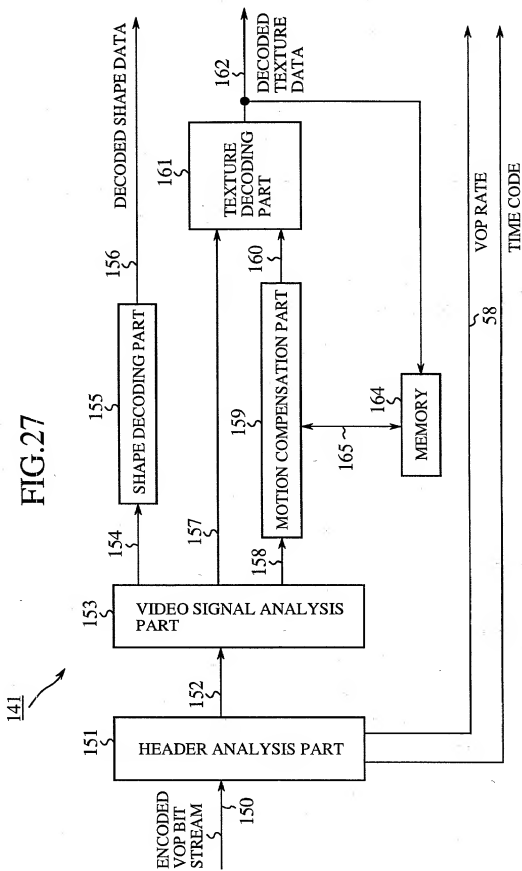


FIG.28

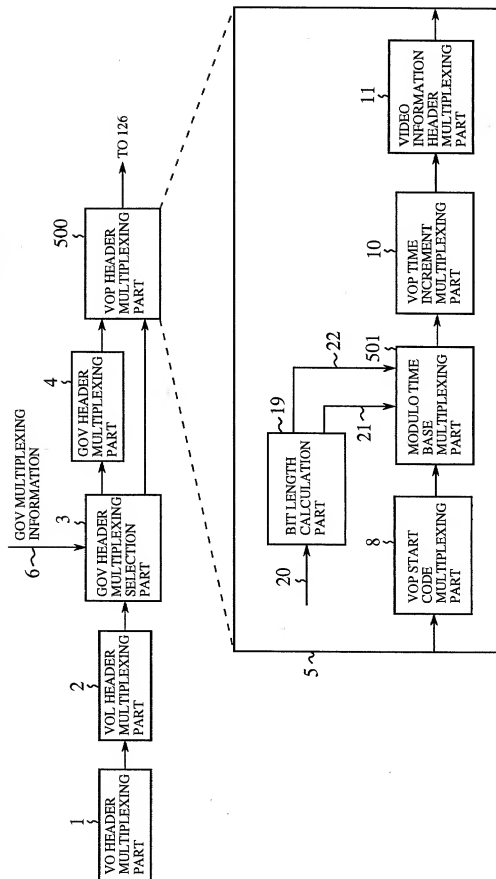


FIG.29

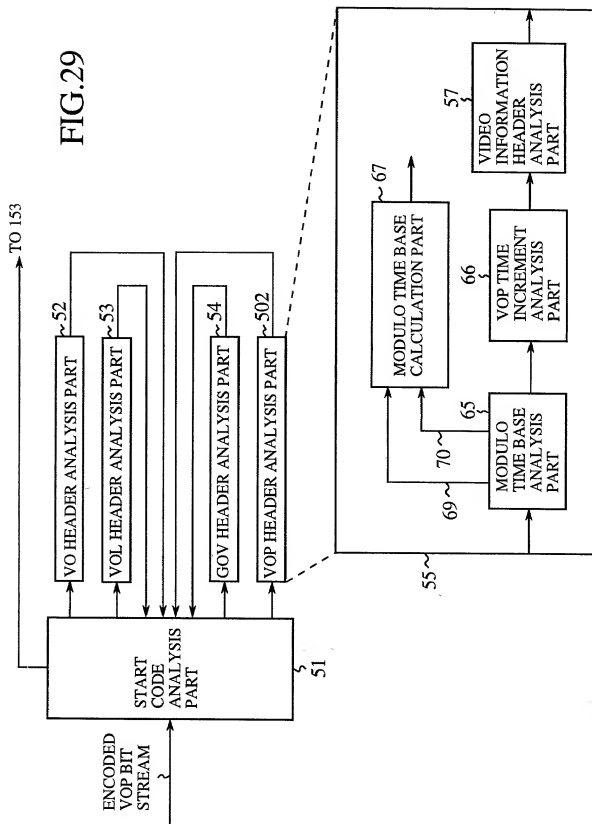


FIG.30

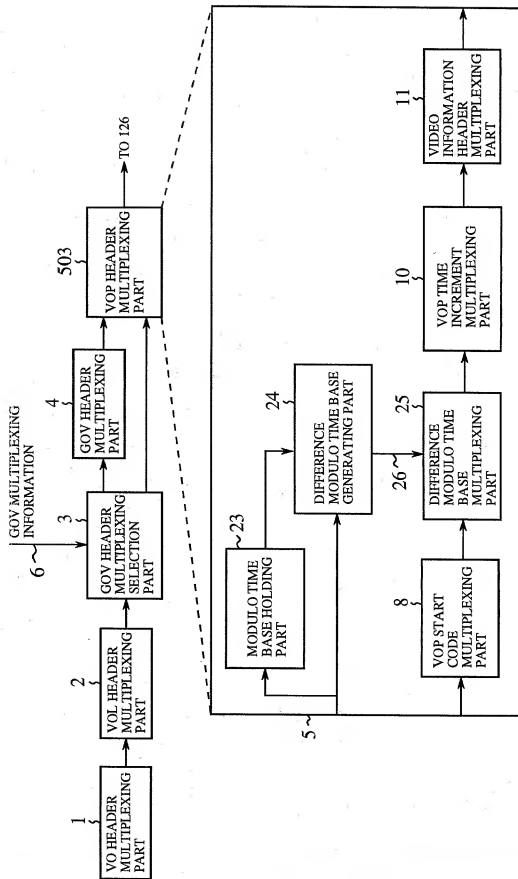
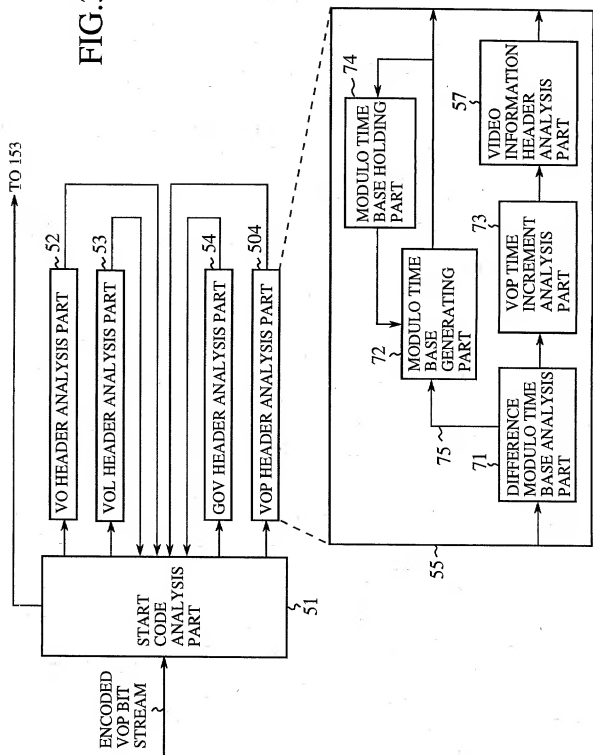


FIG.31



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Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

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My residence, post office address and citizenship are as stated next to my name.

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I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

"IMAGE ENCODING DEVICE AND IMAGE

DECODING DEVICE"

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the specification of which is attached hereto unless the following box is checked:

☐ 月 日 に提出され、米国出願番号または特許協定条約国際出願番号を _____ とし、
 （該当する場合） _____ に訂正されました。

☐ was filed on _____
 as United States Application Number or
 PCT International Application Number
 _____ and was amended on
 _____ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

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I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration

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Prior Foreign Application(s)

外国での先行出願

PCT/JP97/03785

(Number)

(番号)

PCT

(Country)

(国名)

20/October/1997

(Day/Month/Year Filed)

(出願年月日)

Priority Not Claimed

優先権主張なし

☐

(Number)

(番号)

(Country)

(国名)

(Day/Month/Year Filed)

(出願年月日)

☐

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(Application No.)

(出願番号)

(Filing Date)

(出願日)

(Application No.)

(出願番号)

(Filing Date)

(出願日)

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PCT/JP98/00941

(Application No.)

(出願番号)

March 6, 1998

(Filing Date)

(出願日)

(Application No.)

(出願番号)

(Filing Date)

(出願日)

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pending

(Status: Patented, Pending, Abandoned)

(現況: 特許許可済、係属中、放棄済)

(Status: Patented, Pending, Abandoned)

(現況: 特許許可済、係属中、放棄済)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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RAYMOND C. STEWART (Reg. No. 21,066)
JOSEPH A. KOLASCH (Reg. No. 22,483)
ANTHONY L. BIRCH (Reg. No. 26,122)

JAMES M. SLATTERY (Reg. No. 28,380)
BERNARD L. SWEENEY (Reg. No. 24,448)
MICHAEL K. MUTTER (Reg. No. 28,609)
CHARLES GURENSTEIN (Reg. No. 29,271)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)

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TERRY L. CLARK (Reg. No. 32,644)
ANDREW D. MEIKLE (Reg. No. 32,868)

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唯一または第一発明者名	Full name of sole or first inventor Shinichi KURODA	
発明者の署名	日付	Inventor's signature Date Shinichi Kuroda March 6, 2000
住所	Residence Tokyo, Japan	
国籍	Citizenship Japanese	
私書箱	Post Office Address c/o MITSUBISHI DENKI KABUSHIKI KAISHA, 2-3, Marunouchi 2-chome, Chiyoda-ku, Tokyo 100-8310 Japan	
第二共同発明者	Full name of second joint inventor, if any Shunichi SEKIGUCHI	
第二共同発明者	日付	Second inventor's signature Date Shunichi Sekiguchi March 6, 2000
住所	Residence Tokyo, Japan	
国籍	Citizenship Japanese	
私書箱	Post Office Address c/o MITSUBISHI DENKI KABUSHIKI KAISHA, 2-3, Marunouchi 2-chome, Chiyoda-ku, Tokyo 100-8310 Japan	

(第三以降の共同発明者についても同様に記載し、署名をすること)

(Supply similar information and signature for third and subsequent joint inventors.)

第3の共同発明者の氏名 (該当する場合)		Full name of third joint inventor, if any, Kohtaro ASAI	
同第3発明者の署名	日付	Third Inventor's signature <i>Kohtaro Asai</i>	Date March 6, 2000
住所		Residence Tokyo, Japan	
国籍		Citizenship Japanese	
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第4の共同発明者の氏名 (該当する場合)		Full name of fourth joint inventor, if any, Hirofumi NISHIKAWA	
同第4発明者の署名	日付	Fourth Inventor's signature <i>Hirofumi Nishikawa</i>	Date March 6, 2000
住所		Residence Tokyo, Japan	
国籍		Citizenship Japanese	
郵便の宛先		Post Office Address c/o MITSUBISHI DENKI KABUSHIKI KAISHA, 2-3, Marunouchi 2-chome, Chiyoda-ku, Tokyo 100-8310 Japan	
第5の共同発明者の氏名 (該当する場合)		Full name of fifth joint inventor, if any, Yoshimi ISU	
同第5発明者の署名	日付	Fifth Inventor's signature <i>Yoshimi Isu</i>	Date March 6, 2000
住所		Residence Tokyo, Japan	
国籍		Citizenship Japanese	
郵便の宛先		Post Office Address c/o MITSUBISHI DENKI KABUSHIKI KAISHA, 2-3, Marunouchi 2-chome, Chiyoda-ku, Tokyo 100-8310 Japan	

第6の共同発明者の氏名 (該当する場合)		Full name of sixth joint inventor, if any, Yuri HASEGAWA	
同第6発明者の署名	日付	Sixth Inventor's signature Yuri Hasegawa	Date March 6, 2000
住所		Residence Tokyo, Japan	
国籍		Citizenship Japanese	
郵便の宛先		Post Office Address c/o MITSUBISHI DENKI KABUSHIKI KAISHA, 2-3, Marunouchi 2-chome, Chiyoda-ku, Tokyo 100-8310 Japan	
第7の共同発明者の氏名 (該当する場合)		Full name of seventh joint inventor, if any,	
同第7発明者の署名	日付	Seventh Inventor's signature	Date
住所		Residence	
国籍		Citizenship	
郵便の宛先		Post Office Address	
第8の共同発明者の氏名 (該当する場合)		Full name of eighth joint inventor, if any,	
同第8発明者の署名	日付	Eighth Inventor's signature	Date
住所		Residence	
国籍		Citizenship	
郵便の宛先		Post Office Address	